

Software: ART Driver Browser Vers.

Test log begin
Script
version:1.15

Test# PASS/MinMaxReadDescription

..... .FAIL ...Value ...Value ...Value.....

2)	PASS	62	62	62	Dec	PS0_type
3)	PASS	62	62	62	Dec	PS1_type
4)	PASS	62	62	62	Dec	PS2_type
5)	PASS	62	62	62	Dec	PS3_type
6)	PASS	62	62	62	Dec	PS4_type
7)	PASS	62	62	62	Dec	PS5_type
8)	PASS	1	1	1	Dec	Bib_present
9)	PASS	1023	1023	1023	Dec	Electrical_P/N_matching
10)	PASS	31	31	31	Dec	Electrical_S/C_matching
11)	PASS	1	4095	9	Dec	Electrical_S/N_matching
12)	PASS	9000	15000	12010	mV	PS4_as_JIG_Supply_voltage
13)	PASS	300	2500	1316	mA	PS4_as_JIG_Supply_current
14)	PASS	80	80	80	Hex	Bib_databus
15)	PASS	40	40	40	Hex	Bib_databus
16)	PASS	20	20	20	Hex	Bib_databus
17)	PASS	10	10	10	Hex	Bib_databus
18)	PASS	8	8	8	Hex	Bib_databus
19)	PASS	4	4	4	Hex	Bib_databus
20)	PASS	2	2	2	Hex	Bib_databus
21)	PASS	1	1	1	Hex	Bib_databus
22)	PASS	1	1	1	Hex	Bib_Addressbus
23)	PASS	2	2	2	Hex	Bib_Addressbus
24)	PASS	4	4	4	Hex	Bib_Addressbus
25)	PASS	8	8	8	Hex	Bib_Addressbus
26)	PASS	10	10	10	Hex	Bib_Addressbus
27)	PASS	20	20	20	Hex	Bib_Addressbus
28)	PASS	40	40	40	Hex	Bib_Addressbus
29)	PASS	80	80	80	Hex	Bib_Addressbus
30)	PASS	14250	15750	14908	mV	MVB_15V_internal_voltage
31)	PASS	12350	13650	12884	mV	MVB_13V_internal_voltage
32)	PASS	11257	12442	11831	mV	MVB_12V_internal_voltage
33)	PASS	9450	10450	9857	mV	MVB_10V_internal_voltage
34)	PASS	4750	5250	4977	mV	MVB_5V_internal_voltage
35)	PASS	3135	3465	3249	mV	MVB_3V3_internal_voltage
36)	PASS	1710	1890	1793	mV	MVB_1V8_internal_voltage
37)	PASS	-2100	-1900	-2015	mV	MVB_minus_2V_internal_voltage
38)	PASS	-5250	-4750	-4924	mV	MVB_minus_5V_internal_voltage
39)	PASS	-15750	-14250	-14961	mV	MVB_minus_15V_internal_voltage
40)	PASS	2375	2625	2496	mV	MVB_DAC_ref_voltage

41)PASS	-50	50	-1	mVMB_GND_voltage
42)PASS	3135	3465	3233	mVLVPPS_3V3_internal_voltage
43)PASS	1710	1890	1799	mVLVPPS_1V8_internal_voltage
44)PASS	4750	5250	5076	mVLVPPS_5V_internal_voltage
45)PASS	-5250	-4750	-5072	mVLVPPS_minus_5V_internal_voltage
46)PASS	2375	2625	2492	mVLVPPS_2V5_internal_voltage
47)PASS	1710	1890	1798	mVLVPPS_1V8_internal_voltage
48)PASS	-50	50	2	mVLVPPS_DGND_voltage
49)PASS	-50	50	0	mVLVPPS_AGND_voltage
50)PASS	0	0	0	Declvpps_error_code
51)PASS	1	1	1	HexBibCode_Sense_(BCDrive0)
52)PASS	3	3	3	HexBibCode_Sense_(BCDrive1)
53)PASS	6	6	6	HexBibCode_Sense_(BCDrive2)
54)PASS	c	c	c	HexBibCode_Sense_(BCDrive3)
55)PASS	18	18	18	HexBibCode_Sense_(BCDrive4)
56)PASS	11	11	11	HexBibCode_Sense_(BCDrive5)
57)PASS	aa	aa	aa	HexBIB_eeeprom
58)PASS	55	55	55	HexBIB_eeeprom
59)PASS	3c	3c	3c	HexBIB_eeeprom
60)PASS	c3	c3	c3	HexBIB_eeepromPS_0TESTS
101)PASS	46500	53000	47786	mVPS0_Input_Voltage
102)PASS	-350	350	115	mA
PS0_current_offset Voltage programming					
andcurrent					
103) PASS	885	1325	1093	mA PS0_DRV_reread_current
104) PASS	740	860	795	mV PS0_JIG_reread_voltage
105) PASS	485	1105	869	mV PS0_DRV_out_reread_voltage
106) PASS	735	855	804	mV PS0_DRV_sensing_reread_voltage
107) PASS	0	0	0	Dec PS0_Current_Limiter_State
108) PASS	2505	2945	2704	mA PS0_DRV_reread_current
109) PASS	1740	1860	1795	mV PS0_JIG_reread_voltage
110) PASS	1485	2105	1936	mV PS0_DRV_out_reread_voltage
111) PASS	1735	1855	1802	mV PS0_DRV_sensing_reread_voltage
112) PASS	0	0	0	Dec PS0_Current_Limiter_State
113) PASS	0	50	5	mV PS0_Ripple
114) PASS	143	583	358	mA PS0_DRV_reread_current
115) PASS	2640	2760	2700	mV PS0_JIG_reread_voltage
116) PASS	2390	3010	2729	mV PS0_DRV_out_reread_voltage
117) PASS	2640	2760	2697	mV PS0_DRV_sensing_reread_voltage
118) PASS	0	0	0	Dec PS0_Current_Limiter_State
119) PASS	0	50	5	mV PS0_Ripple
120) PASS	224	664	433	mA PS0_DRV_reread_current
121) PASS	3240	3360	3303	mV PS0_JIG_reread_voltage
122) PASS	2993	3613	3339	mV PS0_DRV_out_reread_voltage
123) PASS	3243	3363	3303	mV PS0_DRV_sensing_reread_voltage
124) PASS	0	0	0	Dec PS0_Current_Limiter_State
125) PASS	453	893	660	mA PS0_DRV_reread_current
126) PASS	4940	5060	5006	mV PS0_JIG_reread_voltage
127) PASS	4696	5316	5054	mV PS0_DRV_out_reread_voltage
128) PASS	4946	5066	5000	mV PS0_DRV_sensing_reread_voltage
129) PASS	0	0	0	Dec PS0_Current_Limiter_State
130) PASS	1400	1840	1617	mA PS0_DRV_reread_current
131) PASS	11940	12060	11986	mV PS0_JIG_reread_voltage
132) PASS	11676	12296	12064	mV PS0_DRV_out_reread_voltage
133) PASS	11926	12046	11999	mV PS0_DRV_sensing_reread_voltage
134) PASS	0	0	0	Dec PS0_Current_Limiter_State
135) PASS	2226	2666	2457	mA PS0_DRV_reread_current
136) PASS	17940	18060	17991	mV PS0_JIG_reread_voltage

137)	PASS	17681	18301	18111	mV	PS0_DRV_out_reread_voltage
138)	PASS	17931	18051	18007	mV	PS0_DRV_sensing_reread_voltage
139)	PASS	0	0	0	Dec	PS0_Current_Limiter_State
140)	PASS	0	50	9	mV	PS0_Ripple
141)	PASS	-3257	-2817	-3227	mA	PS0_DRV_reread_current
142)	PASS	-2060	-1940	-1979	mV	PS0_JIG_reread_voltage
143)	PASS	-2289	-1669	-2093	mV	PS0_DRV_out_reread_voltage
144)	PASS	-2039	-1919	-1979	mV	PS0_DRV_sensing_reread_voltage
145)	PASS	0	0	0	Dec	PS0_Current_Limiter_State
146)	PASS	0	50	1	mV	PS0_Ripple
147)	PASS	-890	-450	-695	mA	PS0_DRV_reread_current
148)	PASS	-5060	-4940	-5011	mV	PS0_JIG_reread_voltage
149)	PASS	-5321	-4701	-5018	mV	PS0_DRV_out_reread_voltage
150)	PASS	-5071	-4951	-5000	mV	PS0_DRV_sensing_reread_voltage
151)	PASS	0	0	0	Dec	PS0_Current_Limiter_State
152)	PASS	0	50	6	mV	PS0_Ripple
153)	PASS	-1846	-1406	-1726	mA	PS0_DRV_reread_current
154)	PASS	-12060	-11940	-12022	mV	PS0_JIG_reread_voltage
155)	PASS	-12332	-11712	-12050	mV	PS0_DRV_out_reread_voltage
156)	PASS	-12082	-11962	-11999	mV	PS0_DRV_sensing_reread_voltage
157)	PASS	0	0	0	Dec	PS0_Current_Limiter_State
158)	PASS	-2670	-2230	-2605	mA	PS0_DRV_reread_current
159)	PASS	-18060	-17940	-18007	mV	PS0_JIG_reread_voltage
160)	PASS	-18317	-17697	-18052	mV	PS0_DRV_out_reread_voltage
161)	PASS	-18067	-17947	-17976	mV	PS0_DRV_sensing_reread_voltage
162)	PASS	0	0	0	Dec	PS0_Current_Limiter_State

163)	PASS	0	50	2	mV	PS0_Ripple
Current programming and discharge							
164)	PASS	0	50	7	mV	PS0_Ripple
165)	PASS	1030	1470	1287	mA	PS0_JIG_reread_current
166)	PASS	1067	1507	1271	mA	PS0_DRV_reread_current
167)	PASS	1	1	1	Dec	PS0_Current_Limiter_State
168)	PASS	0	50	9	mV	PS0_Ripple
169)	PASS	6280	6720	6551	mA	PS0_JIG_reread_current
170)	PASS	6331	6771	6495	mA	PS0_DRV_reread_current
171)	PASS	1	1	1	Dec	PS0_Current_Limiter_State
172)	PASS	600	1900	1432	mA	PS0_discharge_current
173)	PASS	10	78	24	°C	PS0_Temperature
PS_1 TESTS							
201)	PASS	46500	53000	47955	mV	PS1_Input_Voltage
202)	PASS	-350	350	22	mA	PS1_current_offset
Voltage programming and current							
203)	PASS	935	1375	1161	mA	PS1_DRV_reread_current
204)	PASS	740	860	795	mV	PS1_JIG_reread_voltage
205)	PASS	485	1105	858	mV	PS1_DRV_out_reread_voltage
206)	PASS	735	855	798	mV	PS1_DRV_sensing_reread_voltage
207)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
208)	PASS	2499	2939	2725	mA	PS1_DRV_reread_current
209)	PASS	1740	1860	1794	mV	PS1_JIG_reread_voltage
210)	PASS	1484	2104	1927	mV	PS1_DRV_out_reread_voltage
211)	PASS	1734	1854	1798	mV	PS1_DRV_sensing_reread_voltage
212)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
213)	PASS	0	50	4	mV	PS1_Ripple
214)	PASS	141	581	353	mA	PS1_DRV_reread_current
215)	PASS	2640	2760	2703	mV	PS1_JIG_reread_voltage
216)	PASS	2393	3013	2729	mV	PS1_DRV_out_reread_voltage
217)	PASS	2643	2763	2699	mV	PS1_DRV_sensing_reread_voltage
218)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
219)	PASS	0	50	4	mV	PS1_Ripple
220)	PASS	221	661	432	mA	PS1_DRV_reread_current

221)	PASS	3240	3360	3302	mV	PS1_JIG_reread_voltage
222)	PASS	2992	3612	3333	mV	PS1_DRV_out_reread_voltage
223)	PASS	3242	3362	3300	mV	PS1_DRV_sensing_reread_voltage
224)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
225)	PASS	450	890	662	mA	PS1_DRV_reread_current
226)	PASS	4940	5060	5007	mV	PS1_JIG_reread_voltage
227)	PASS	4697	5317	5052	mV	PS1_DRV_out_reread_voltage
228)	PASS	4947	5067	5002	mV	PS1_DRV_sensing_reread_voltage
229)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
230)	PASS	1403	1843	1629	mA	PS1_DRV_reread_current
231)	PASS	11940	12060	11992	mV	PS1_JIG_reread_voltage
232)	PASS	11682	12302	12067	mV	PS1_DRV_out_reread_voltage
233)	PASS	11932	12052	11999	mV	PS1_DRV_sensing_reread_voltage
234)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
235)	PASS	2231	2671	2471	mA	PS1_DRV_reread_current
236)	PASS	17940	18060	18004	mV	PS1_JIG_reread_voltage
237)	PASS	17694	18314	18117	mV	PS1_DRV_out_reread_voltage
238)	PASS	17944	18064	18011	mV	PS1_DRV_sensing_reread_voltage
239)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
240)	PASS	0	50	8	mV	PS1_Ripple
241)	PASS	-3282	-2842	-3227	mA	PS1_DRV_reread_current
242)	PASS	-2060	-1940	-1995	mV	PS1_JIG_reread_voltage
243)	PASS	-2305	-1685	-2110	mV	PS1_DRV_out_reread_voltage
244)	PASS	-2055	-1935	-2003	mV	PS1_DRV_sensing_reread_voltage
245)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
246)	PASS	0	50	6	mV	PS1_Ripple
247)	PASS	-890	-450	-708	mA	PS1_DRV_reread_current
248)	PASS	-5060	-4940	-5011	mV	PS1_JIG_reread_voltage
249)	PASS	-5321	-4701	-5019	mV	PS1_DRV_out_reread_voltage
250)	PASS	-5071	-4951	-4999	mV	PS1_DRV_sensing_reread_voltage
251)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
252)	PASS	0	50	5	mV	PS1_Ripple
253)	PASS	-1848	-1408	-1727	mA	PS1_DRV_reread_current
254)	PASS	-12060	-11940	-12019	mV	PS1_JIG_reread_voltage
255)	PASS	-12329	-11709	-12060	mV	PS1_DRV_out_reread_voltage
256)	PASS	-12079	-11959	-11999	mV	PS1_DRV_sensing_reread_voltage
257)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
258)	PASS	-2675	-2235	-2607	mA	PS1_DRV_reread_current
259)	PASS	-18060	-17940	-18016	mV	PS1_JIG_reread_voltage
260)	PASS	-18326	-17706	-18071	mV	PS1_DRV_out_reread_voltage
261)	PASS	-18076	-17956	-18001	mV	PS1_DRV_sensing_reread_voltage
262)	PASS	0	0	0	Dec	PS1_Current_Limiter_State
263)	PASS	0	50	8	mV	PS1_Ripple
Current programming and discharge							
264)	PASS	0	50	5	mV	PS1_Ripple
265)	PASS	1030	1470	1278	mA	PS1_JIG_reread_current
266)	PASS	1058	1498	1273	mA	PS1_DRV_reread_current
267)	PASS	1	1	1	Dec	PS1_Current_Limiter_State
268)	PASS	0	50	9	mV	PS1_Ripple
269)	PASS	6280	6720	6498	mA	PS1_JIG_reread_current
270)	PASS	6278	6718	6493	mA	PS1_DRV_reread_current
271)	PASS	1	1	1	Dec	PS1_Current_Limiter_State
272)	PASS	600	1900	1414	mA	PS1_discharge_current
273)	PASS	10	78	23	°C	PS1_Temperature
PS_2 TESTS							
301)	PASS	46500	53000	48021	mV	PS2_Input_Voltage
302)	PASS	-350	350	34	mA	PS2_current_offset
Voltage programming and current							
303)	PASS	965	1405	1183	mA	PS2_DRV_reread_current
304)	PASS	740	860	794	mV	PS2_JIG_reread_voltage

305)	PASS	484	1104	864	mV	PS2_DRV_out_reread_voltage
306)	PASS	734	854	798	mV	PS2_DRV_sensing_reread_voltage
307)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
308)	PASS	2509	2949	2732	mA	PS2_DRV_reread_current
309)	PASS	1740	1860	1792	mV	PS2_JIG_reread_voltage
310)	PASS	1482	2102	1949	mV	PS2_DRV_out_reread_voltage
311)	PASS	1732	1852	1802	mV	PS2_DRV_sensing_reread_voltage
312)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
313)	PASS	0	50	5	mV	PS2_Ripple
314)	PASS	142	582	345	mA	PS2_DRV_reread_current
315)	PASS	2640	2760	2705	mV	PS2_JIG_reread_voltage
316)	PASS	2395	3015	2726	mV	PS2_DRV_out_reread_voltage
317)	PASS	2645	2765	2700	mV	PS2_DRV_sensing_reread_voltage
318)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
319)	PASS	0	50	6	mV	PS2_Ripple
320)	PASS	223	663	431	mA	PS2_DRV_reread_current
321)	PASS	3240	3360	3304	mV	PS2_JIG_reread_voltage
322)	PASS	2994	3614	3333	mV	PS2_DRV_out_reread_voltage
323)	PASS	3244	3364	3301	mV	PS2_DRV_sensing_reread_voltage
324)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
325)	PASS	452	892	660	mA	PS2_DRV_reread_current
326)	PASS	4940	5060	5007	mV	PS2_JIG_reread_voltage
327)	PASS	4697	5317	5053	mV	PS2_DRV_out_reread_voltage
328)	PASS	4947	5067	5005	mV	PS2_DRV_sensing_reread_voltage
329)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
330)	PASS	1403	1843	1622	mA	PS2_DRV_reread_current
331)	PASS	11940	12060	11990	mV	PS2_JIG_reread_voltage
332)	PASS	11680	12300	12073	mV	PS2_DRV_out_reread_voltage
333)	PASS	11930	12050	11986	mV	PS2_DRV_sensing_reread_voltage
334)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
335)	PASS	2232	2672	2465	mA	PS2_DRV_reread_current
336)	PASS	17940	18060	17993	mV	PS2_JIG_reread_voltage
337)	PASS	17683	18303	18132	mV	PS2_DRV_out_reread_voltage
338)	PASS	17933	18053	17999	mV	PS2_DRV_sensing_reread_voltage
339)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
340)	PASS	0	50	10	mV	PS2_Ripple
341)	PASS	-3273	-2833	-3203	mA	PS2_DRV_reread_current
342)	PASS	-2060	-1940	-1998	mV	PS2_JIG_reread_voltage
343)	PASS	-2308	-1688	-2140	mV	PS2_DRV_out_reread_voltage
344)	PASS	-2058	-1938	-1999	mV	PS2_DRV_sensing_reread_voltage
345)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
346)	PASS	0	50	4	mV	PS2_Ripple
347)	PASS	-887	-447	-696	mA	PS2_DRV_reread_current
348)	PASS	-5060	-4940	-5011	mV	PS2_JIG_reread_voltage
349)	PASS	-5321	-4701	-5029	mV	PS2_DRV_out_reread_voltage
350)	PASS	-5071	-4951	-4996	mV	PS2_DRV_sensing_reread_voltage
351)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
352)	PASS	0	50	6	mV	PS2_Ripple
353)	PASS	-1848	-1408	-1713	mA	PS2_DRV_reread_current
354)	PASS	-12060	-11940	-12008	mV	PS2_JIG_reread_voltage
355)	PASS	-12318	-11698	-12060	mV	PS2_DRV_out_reread_voltage
356)	PASS	-12068	-11948	-11999	mV	PS2_DRV_sensing_reread_voltage
357)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
358)	PASS	-2676	-2236	-2597	mA	PS2_DRV_reread_current
359)	PASS	-18060	-17940	-18007	mV	PS2_JIG_reread_voltage
360)	PASS	-18317	-17697	-18093	mV	PS2_DRV_out_reread_voltage
361)	PASS	-18067	-17947	-18007	mV	PS2_DRV_sensing_reread_voltage
362)	PASS	0	0	0	Dec	PS2_Current_Limiter_State
363)	PASS	0	50	10	mV	PS2_Ripple

Current programming and discharge

364)	PASS	0	50	6 mV	PS2_Ripple
365)	PASS	1030	1470	1276 mA	PS2_JIG_reread_current
366)	PASS	1056	1496	1275 mA	PS2_DRV_reread_current
367)	PASS	1	1	1 Dec	PS2_Current_Limiter_State
368)	PASS	0	50	6 mV	PS2_Ripple
369)	PASS	6280	6720	6514 mA	PS2_JIG_reread_current
370)	PASS	6294	6734	6521 mA	PS2_DRV_reread_current
371)	PASS	1	1	1 Dec	PS2_Current_Limiter_State
372)	PASS	600	1900	1440 mA	PS2_discharge_current
373)	PASS	10	78	24 0C	PS2_Temperature
PS_3 TESTS					
401)	PASS	46500	53000	48272 mV	PS3_Input_Voltage
402)	PASS	-350	350	56 mA	PS3_current_offset
Voltage programming and current					
403)	PASS	887	1327	1110 mA	PS3_DRV_reread_current
404)	PASS	740	860	796 mV	PS3_JIG_reread_voltage
405)	PASS	486	1106	870 mV	PS3_DRV_out_reread_voltage
406)	PASS	736	856	799 mV	PS3_DRV_sensing_reread_voltage
407)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
408)	PASS	2492	2932	2718 mA	PS3_DRV_reread_current
409)	PASS	1740	1860	1792 mV	PS3_JIG_reread_voltage
410)	PASS	1482	2102	1962 mV	PS3_DRV_out_reread_voltage
411)	PASS	1732	1852	1799 mV	PS3_DRV_sensing_reread_voltage
412)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
413)	PASS	0	50	7 mV	PS3_Ripple
414)	PASS	141	581	357 mA	PS3_DRV_reread_current
415)	PASS	2640	2760	2702 mV	PS3_JIG_reread_voltage
416)	PASS	2392	3012	2729 mV	PS3_DRV_out_reread_voltage
417)	PASS	2642	2762	2700 mV	PS3_DRV_sensing_reread_voltage
418)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
419)	PASS	0	50	6 mV	PS3_Ripple
420)	PASS	222	662	441 mA	PS3_DRV_reread_current
421)	PASS	3240	3360	3305 mV	PS3_JIG_reread_voltage
422)	PASS	2995	3615	3336 mV	PS3_DRV_out_reread_voltage
423)	PASS	3245	3365	3303 mV	PS3_DRV_sensing_reread_voltage
424)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
425)	PASS	449	889	674 mA	PS3_DRV_reread_current
426)	PASS	4940	5060	5007 mV	PS3_JIG_reread_voltage
427)	PASS	4697	5317	5048 mV	PS3_DRV_out_reread_voltage
428)	PASS	4947	5067	4999 mV	PS3_DRV_sensing_reread_voltage
429)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
430)	PASS	1405	1845	1639 mA	PS3_DRV_reread_current
431)	PASS	11940	12060	11998 mV	PS3_JIG_reread_voltage
432)	PASS	11688	12308	12085 mV	PS3_DRV_out_reread_voltage
433)	PASS	11938	12058	11997 mV	PS3_DRV_sensing_reread_voltage
434)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
435)	PASS	2233	2673	2484 mA	PS3_DRV_reread_current
436)	PASS	17940	18060	17997 mV	PS3_JIG_reread_voltage
437)	PASS	17687	18307	18131 mV	PS3_DRV_out_reread_voltage
438)	PASS	17937	18057	18009 mV	PS3_DRV_sensing_reread_voltage
439)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
440)	PASS	0	50	16 mV	PS3_Ripple
441)	PASS	-3289	-2849	-3206 mA	PS3_DRV_reread_current
442)	PASS	-2060	-1940	-1996 mV	PS3_JIG_reread_voltage
443)	PASS	-2306	-1686	-2160 mV	PS3_DRV_out_reread_voltage
444)	PASS	-2056	-1936	-2000 mV	PS3_DRV_sensing_reread_voltage
445)	PASS	0	0	0 Dec	PS3_Current_Limiter_State
446)	PASS	0	50	6 mV	PS3_Ripple
447)	PASS	-895	-455	-700 mA	PS3_DRV_reread_current

448) PASS	-5060	-4940	-5011 mV	PS3_JIG_reread_voltage
449) PASS	-5321	-4701	-5036 mV	PS3_DRV_out_reread_voltage
450) PASS	-5071	-4951	-5000 mV	PS3_DRV_sensing_reread_voltage
451) PASS	0	0	0 Dec	PS3_Current_Limiter_State
452) PASS	0	50	7 mV	PS3_Ripple
453) PASS	-1847	-1407	-1705 mA	PS3_DRV_reread_current
454) PASS	-12060	-11940	-11994 mV	PS3_JIG_reread_voltage
455) PASS	-12304	-11684	-12059 mV	PS3_DRV_out_reread_voltage
456) PASS	-12054	-11934	-11995 mV	PS3_DRV_sensing_reread_voltage
457) PASS	0	0	0 Dec	PS3_Current_Limiter_State
458) PASS	-2674	-2234	-2579 mA	PS3_DRV_reread_current
459) PASS	-18060	-17940	-17994 mV	PS3_JIG_reread_voltage
460) PASS	-18304	-17684	-18087 mV	PS3_DRV_out_reread_voltage
461) PASS	-18054	-17934	-17999 mV	PS3_DRV_sensing_reread_voltage
462) PASS	0	0	0 Dec	PS3_Current_Limiter_State
463) PASS	0	50	8 mV	PS3_Ripple
Current programming and discharge					
464)PASS	0	50	7 mV	PS3_Ripple
465) PASS	1030	1470	1287 mA	PS3_JIG_reread_current
466) PASS	1067	1507	1286 mA	PS3_DRV_reread_current
467) PASS	1	1	1 Dec	PS3_Current_Limiter_State
468) PASS	0	50	8 mV	PS3_Ripple
469) PASS	6280	6720	6503 mA	PS3_JIG_reread_current
470) PASS	6283	6723	6520 mA	PS3_DRV_reread_current
471) PASS	1	1	1 Dec	PS3_Current_Limiter_State
472) PASS	600	1900	1402 mA	PS3_discharge_current
473) PASS	10	78	24 0C	PS3_Temperature
501) PASS	9000	15000	11978 mV	PS1_as_JIG_Supply_voltage
502) PASS	300	2500	1471 mA	PS1_as_JIG_Supply_current
PS_4 TESTS					
503) PASS	46500	53000	48190 mV	PS4_Input_Voltage
504) PASS	-350	350	122 mA	PS4_current_offset
Voltage programming and current					
505) PASS	901	1341	1115 mA	PS4_DRV_reread_current
506) PASS	740	860	793 mV	PS4_JIG_reread_voltage
507) PASS	483	1103	822 mV	PS4_DRV_out_reread_voltage
508) PASS	733	853	800 mV	PS4_DRV_sensing_reread_voltage
509)PASS	0	0	0 Dec	PS4_Current_Limiter_State
510)PASS	2475	2915	2696 mA	PS4_DRV_reread_current
511)PASS	1740	1860	1792 mV	PS4_JIG_reread_voltage
512)PASS	1482	2102	1857 mV	PS4_DRV_out_reread_voltage
513)PASS	1732	1852	1800 mV	PS4_DRV_sensing_reread_voltage
514)PASS	0	0	0 Dec	PS4_Current_Limiter_State
515)PASS	0	50	5 mV	PS4_Ripple
516)PASS	142	582	352 mA	PS4_DRV_reread_current
517)PASS	2640	2760	2702 mV	PS4_JIG_reread_voltage
518)PASS	2392	3012	2701 mV	PS4_DRV_out_reread_voltage
519)PASS	2642	2762	2699 mV	PS4_DRV_sensing_reread_voltage
520)PASS	0	0	0 Dec	PS4_Current_Limiter_State
521)PASS	0	50	6 mV	PS4_Ripple
522)PASS	222	662	431 mA	PS4_DRV_reread_current
523)PASS	3240	3360	3301 mV	PS4_JIG_reread_voltage
524)PASS	2991	3611	3312 mV	PS4_DRV_out_reread_voltage
525)PASS	3241	3361	3302 mV	PS4_DRV_sensing_reread_voltage
526)PASS	0	0	0 Dec	PS4_Current_Limiter_State
527)PASS	451	891	665 mA	PS4_DRV_reread_current
528)PASS	4940	5060	5005 mV	PS4_JIG_reread_voltage
529)PASS	4695	5315	5014 mV	PS4_DRV_out_reread_voltage

530)PASS	4945	5065	5000	mVPS4_DRV_sensing_reread_voltage
531)PASS	0	0	0	DecPS4_Current_Limiter_State
532)PASS	1405	1845	1634	mAPS4_DRV_reread_current
533)PASS	11940	12060	11998	mVPS4_JIG_reread_voltage
534)PASS	11688	12308	12025	mVPS4_DRV_out_reread_voltage
535)PASS	11938	12058	11990	mVPS4_DRV_sensing_reread_voltage
536)PASS	0	0	0	DecPS4_Current_Limiter_State
537)PASS	2234	2674	2478	mAPS4_DRV_reread_current
538)PASS	17940	18060	17995	mVPS4_JIG_reread_voltage
539)PASS	17685	18305	18055	mVPS4_DRV_out_reread_voltage
540)PASS	17935	18055	18007	mVPS4_DRV_sensing_reread_voltage
541)PASS	0	0	0	DecPS4_Current_Limiter_State
542)PASS	0	50	9	mVPS4_Ripple
543)PASS	-3257	-2817	-3183	mAPS4_DRV_reread_current
544)PASS	-2060	-1940	-1997	mVPS4_JIG_reread_voltage
545)PASS	-2307	-1687	-2070	mVPS4_DRV_out_reread_voltage
546)PASS	-2057	-1937	-1996	mVPS4_DRV_sensing_reread_voltage
547)PASS	0	0	0	DecPS4_Current_Limiter_State
548)PASS	0	50	5	mVPS4_Ripple
549)PASS	-897	-457	-702	mAPS4_DRV_reread_current
550)PASS	-5060	-4940	-5010	mVPS4_JIG_reread_voltage
551)PASS	-5320	-4700	-5024	mVPS4_DRV_out_reread_voltage
552)PASS	-5070	-4950	-5001	mVPS4_DRV_sensing_reread_voltage
553)PASS	0	0	0	DecPS4_Current_Limiter_State
554)PASS	0	50	6	mVPS4_Ripple
555)PASS	-1847	-1407	-1708	mAPS4_DRV_reread_current
556)PASS	-12060	-11940	-11987	mVPS4_JIG_reread_voltage
557)PASS	-12297	-11677	-12016	mVPS4_DRV_out_reread_voltage
558)PASS	-12047	-11927	-11992	mVPS4_DRV_sensing_reread_voltage

559)PASS	0	0	0	DecPS4_Current_Limiter_State
560)PASS	-2675	-2235	-2595	mAPS4_DRV_reread_current
561)PASS	-18060	-17940	-17991	mVPS4_JIG_reread_voltage
562)PASS	-18301	-17681	-18016	mVPS4_DRV_out_reread_voltage
563)PASS	-18051	-17931	-17999	mVPS4_DRV_sensing_reread_voltage
564)PASS	0	0	0	DecPS4_Current_Limiter_State
565)PASS	0	50	9	mVPS4_Ripple

Current programming and discharge

566) PASS	0	50	7	mV PS4_Ripple
567) PASS	1030	1470	1259	mA PS4_JIG_reread_current
568) PASS	1039	1479	1240	mA PS4_DRV_reread_current
569) PASS	1	1	1	Dec PS4_Current_Limiter_State
570) PASS	0	50	9	mV PS4_Ripple
571) PASS	6280	6720	6477	mA PS4_JIG_reread_current
572) PASS	6257	6697	6463	mA PS4_DRV_reread_current
573) PASS	1	1	1	Dec PS4_Current_Limiter_State
574) PASS	600	1900	1358	mA PS4_discharge_current
575) PASS	10	78	26	°C PS4_Temperature
601) PASS	9000	15000	11991	mV PS4_as_JIG_Supply_voltage
602) PASS	300	2500	1427	mA PS4_as_JIG_Supply_current

PS_5 TESTS

603) PASS	46500	53000	47910	mV PS5_Input_Voltage
604) PASS	-350	350	5	mA PS5_current_offset

Voltage programming and current

605) PASS	924	1364	1130	mA PS5_DRV_reread_current
606) PASS	740	860	798	mV PS5_JIG_reread_voltage
607) PASS	488	1108	833	mV PS5_DRV_out_reread_voltage
608) PASS	738	858	804	mV PS5_DRV_sensing_reread_voltage
609) PASS	0	0	0	Dec PS5_Current_Limiter_State
610) PASS	2474	2914	2667	mA PS5_DRV_reread_current
611) PASS	1740	1860	1796	mV PS5_JIG_reread_voltage
612) PASS	1486	2106	1875	mV PS5_DRV_out_reread_voltage
613) PASS	1736	1856	1800	mV PS5_DRV_sensing_reread_voltage
614) PASS	0	0	0	Dec PS5_Current_Limiter_State
615) PASS	0	50	4	mV PS5_Ripple
616) PASS	145	585	349	mA PS5_DRV_reread_current
617) PASS	2640	2760	2706	mV PS5_JIG_reread_voltage
618) PASS	2396	3016	2711	mV PS5_DRV_out_reread_voltage
619) PASS	2646	2766	2699	mV PS5_DRV_sensing_reread_voltage
620) PASS	0	0	0	Dec PS5_Current_Limiter_State
621) PASS	0	50	6	mV PS5_Ripple
622) PASS	226	666	431	mA PS5_DRV_reread_current
623) PASS	3240	3360	3304	mV PS5_JIG_reread_voltage
624) PASS	2994	3614	3320	mV PS5_DRV_out_reread_voltage
625) PASS	3244	3364	3301	mV PS5_DRV_sensing_reread_voltage
626) PASS	0	0	0	Dec PS5_Current_Limiter_State
627) PASS	456	896	662	mA PS5_DRV_reread_current
628) PASS	4940	5060	5009	mV PS5_JIG_reread_voltage
629) PASS	4699	5319	5028	mV PS5_DRV_out_reread_voltage
630) PASS	4949	5069	5003	mV PS5_DRV_sensing_reread_voltage

631)PASS	0	0	0	DecPS5_Current_Limiter_State
632)PASS	1411	1851	1624	mAPS5_DRV_reread_current
633)PASS	11940	12060	12017	mVPS5_JIG_reread_voltage
634)PASS	11707	12327	12066	mVPS5_DRV_out_reread_voltage
635)PASS	11957	12077	12001	mVPS5_DRV_sensing_reread_voltage

636)PASS	0	0	0	DecPS5_Current_Limiter_State
637)PASS	2239	2679	2459	mAPS5_DRV_reread_current
638)PASS	17940	18060	18024	mVPS5_JIG_reread_voltage
639)PASS	17714	18334	18106	mVPS5_DRV_out_reread_voltage
640)PASS	17964	18084	18001	mVPS5_DRV_sensing_reread_voltage
641)PASS	0	0	0	DecPS5_Current_Limiter_State
642)PASS	0	50	8	mVPS5_Ripple
643)PASS	-3256	-2816	-3198	mAPS5_DRV_reread_current
644)PASS	-2060	-1940	-1997	mVPS5_JIG_reread_voltage
645)PASS	-2307	-1687	-2078	mVPS5_DRV_out_reread_voltage
646)PASS	-2057	-1937	-1999	mVPS5_DRV_sensing_reread_voltage
647)PASS	0	0	0	DecPS5_Current_Limiter_State
648)PASS	0	50	7	mVPS5_Ripple
649)PASS	-899	-459	-702	mAPS5_DRV_reread_current
650)PASS	-5060	-4940	-5010	mVPS5_JIG_reread_voltage
651)PASS	-5320	-4700	-5023	mVPS5_DRV_out_reread_voltage
652)PASS	-5070	-4950	-4996	mVPS5_DRV_sensing_reread_voltage
653)PASS	0	0	0	DecPS5_Current_Limiter_State
654)PASS	0	50	6	mVPS5_Ripple
655)PASS	-1849	-1409	-1717	mAPS5_DRV_reread_current
656)PASS	-12060	-11940	-11995	mVPS5_JIG_reread_voltage
657)PASS	-12305	-11685	-12024	mVPS5_DRV_out_reread_voltage
658)PASS	-12055	-11935	-11994	mVPS5_DRV_sensing_reread_voltage
659)PASS	0	0	0	DecPS5_Current_Limiter_State
660)PASS	-2677	-2237	-2609	mAPS5_DRV_reread_current
661)PASS	-18060	-17940	-17995	mVPS5_JIG_reread_voltage
662)PASS	-18305	-17685	-18036	mVPS5_DRV_out_reread_voltage
663)PASS	-18055	-17935	-17996	mVPS5_DRV_sensing_reread_voltage
664)PASS	0	0	0	DecPS5_Current_Limiter_State
665)PASS	0	50	11	mVPS5_Ripple

Current programming and discharge

666) PASS	0	50	7	mV PS5_Ripple
667) PASS	1030	1470	1270	mA PS5_JIG_reread_current
668) PASS	1050	1490	1265	mA PS5_DRV_reread_current
669) PASS	1	1	1	Dec PS5_Current_Limiter_State
670) PASS	0	50	9	mV PS5_Ripple
671) PASS	6280	6720	6538	mA PS5_JIG_reread_current
672) PASS	6318	6758	6485	mA PS5_DRV_reread_current
673) PASS	1	1	1	Dec PS5_Current_Limiter_State
674) PASS	600	1900	1410	mA PS5_discharge_current
675) PASS	10	78	24	0C PS5_Temperature

PS INTERACTION TESTS

701) PASS	-900	900	263	mV
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PS2_out_voltage_when_PS0_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)

702)	PASS	-900	900	261	mV
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PS3_out_voltage_when_PS0_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)703)PASS-900 900 10mV

PS5_out_voltage_when_PS0_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)704)PASS-900 900 220mV

PS0_out_voltage_when_PS2_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)705)PASS-900 900 264mV

PS3_out_voltage_when_PS2_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)706)PASS-900 900 9mV

PS5_out_voltage_when_PS2_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)707)PASS-900	900	216mV
PS0_out_voltage_when_PS3_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)708)PASS-900	900	269mV
PS2_out_voltage_when_PS3_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)709)PASS-900	900	2mV
PS5_out_voltage_when_PS3_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)710)PASS-900	900	215mV
PS0_out_voltage_when_PS5_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)711)PASS-900	900	263mV
PS2_out_voltage_when_PS5_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)712)PASS-900	900	267mV
PS3_out_voltage_when_PS5_on_(VOH4_for_IO0-IO1_VOH1_for_IO2_to_17)713)PASS-900	900	34mV
PS2_out_voltage_when_PS0_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)714)PASS-900	900	209mV
PS3_out_voltage_when_PS0_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)715)PASS-900	900	18mV
PS5_out_voltage_when_PS0_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)716)PASS-900	900	282mV
PS0_out_voltage_when_PS2_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)717)PASS-900	900	210mV
PS3_out_voltage_when_PS2_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)718)PASS-900	900	21mV
PS5_out_voltage_when_PS2_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)719)PASS-900	900	269mV
PS0_out_voltage_when_PS3_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)720)PASS-900	900	32mV
PS2_out_voltage_when_PS3_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)721)PASS-900	900	19mV
PS5_out_voltage_when_PS3_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)722)PASS-900	900	269mV
PS0_out_voltage_when_PS5_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)723)PASS-900	900	23mV
PS2_out_voltage_when_PS5_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)724)PASS-900	900	212mV
PS3_out_voltage_when_PS5_on_(VOH3_for_IO0-IO1_VOH2_for_IO2_to_17)725)PASS-900	900	209mV
PS2_out_voltage_when_PS0_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)726)PASS-900	900	264mV

PS3_out_voltage_when_PS0_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)

727)PASS	-900	900	13mV
PS5_out_voltage_when_PS0_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)728)PASS-900		900	279mV
PS0_out_voltage_when_PS2_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)729)PASS-900		900	267mV
PS3_out_voltage_when_PS2_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)730)PASS-900		900	11mV
PS5_out_voltage_when_PS2_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)731)PASS-900		900	271mV
PS0_out_voltage_when_PS3_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)732)PASS-900		900	221mV
PS2_out_voltage_when_PS3_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)733)PASS-900		900	13mV
PS5_out_voltage_when_PS3_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)734)PASS-900		900	275mV
PS0_out_voltage_when_PS5_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)735)PASS-900		900	213mV
PS2_out_voltage_when_PS5_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)736)PASS-900		900	271mV
PS3_out_voltage_when_PS5_on_(VOH2_for_IO0-IO1_VOH1_for_IO2_to_17)737)PASS-900		900	25mV
PS2_out_voltage_when_PS0_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)738)PASS-900		900	271mV
PS3_out_voltage_when_PS0_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)739)PASS-900		900	18mV
PS5_out_voltage_when_PS0_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)740)PASS-900		900	278mV
PS0_out_voltage_when_PS2_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)741)PASS-900		900	266mV
PS3_out_voltage_when_PS2_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)742)PASS-900		900	17mV
PS5_out_voltage_when_PS2_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)743)PASS-900		900	275mV
PS0_out_voltage_when_PS3_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)744)PASS-900		900	24mV
PS2_out_voltage_when_PS3_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)745)PASS-900		900	21mV
PS5_out_voltage_when_PS3_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17)746)PASS-900		900	271mV

PS0_out_voltage_when_PS5_on_(VOH1_for_IO0-
IO1_VOH2_for_IO2_to_17)747)PASS-900 900 33mV

PS2_out_voltage_when_PS5_on_(VOH1_for_IO0-
IO1_VOH2_for_IO2_to_17)748)PASS-900 900 272mV

PS3_out_voltage_when_PS5_on_(VOH1_for_IO0-IO1_VOH2_for_IO2_to_17) PS
SHARING TESTS

801)PASS 2839 3159 2992mV

PS1_Slave_reread_voltage_(PS0_master)

802)PASS 1044 3144 2316mA

PS1_Slave_reread_current_(PS0_master)

803)PASS 2839 3159 3033mV

PS2_Slave_reread_voltage_(PS1_master)

804)PASS 1011 3111 2429mA

PS2_Slave_reread_current_(PS1_master)

805)PASS 2841 3161 2994mV

PS3_Slave_reread_voltage_(PS2_master)

806)PASS 1243 3343 2119mA

PS3_Slave_reread_current_(PS2_master)

807) PASS 9000 15000 12006 mV PS5_as_JIG_Supply_voltage
808) PASS 300 2500 1513 mA PS5_as_JIG_Supply_current
809) PASS 2841 3161 2919 mV

PS4_Slave_reread_voltage_(PS3_master)

810)PASS 1196 3296 2200mA

PS4_Slave_reread_current_(PS3_master)

811) PASS 9000 15000 12004 mV PS3_as_JIG_Supply_voltage
812) PASS 300 2500 1483 mA PS3_as_JIG_Supply_current
813) PASS 2840 3160 3040 mV

PS5_Slave_reread_voltage_(PS4_master)

814)PASS 947 3047 2548mA

PS5_Slave_reread_current_(PS4_master)

VREF TESTS

901) PASS 8130 14130 11089 uA VRef0_DRV_reread_current
902) PASS 765 835 787 mV VRef0_JIG_reread_voltage
903) PASS 727 847 789 mV VRef0_DRV_reread_voltage
904) PASS 22283 28283 24919 uA VRef0_DRV_reread_current
905) PASS 1765 1835 1789 mV VRef0_JIG_reread_voltage
906) PASS 1729 1849 1794 mV VRef0_DRV_reread_voltage
907) PASS 34954 40954 37556 uA VRef0_DRV_reread_current
908) PASS 2665 2735 2688 mV VRef0_JIG_reread_voltage
909) PASS 2628 2748 2692 mV VRef0_DRV_reread_voltage

910)	PASS	43408	49408	45927	uA	VRef0_DRV_reread_current
911)	PASS	3265	3335	3286	mV	VRef0_JIG_reread_voltage
912)	PASS	3226	3346	3287	mV	VRef0_DRV_reread_voltage
913)	PASS	995	6995	4890	uA	VRef0_DRV_reread_current
914)	PASS	4965	5035	4996	mV	VRef0_JIG_reread_voltage
915)	PASS	4936	5056	4985	mV	VRef0_DRV_reread_voltage
916)	PASS	6609	12609	11688	uA	VRef0_DRV_reread_current
917)	PASS	11965	12035	12013	mV	VRef0_JIG_reread_voltage
918)	PASS	11953	12073	11976	mV	VRef0_DRV_reread_voltage
919)	PASS	-4607	1393	-1944	uA	VRef0_DRV_reread_current
920)	PASS	-2035	-1965	-2009	mV	VRef0_JIG_reread_voltage
921)	PASS	-2069	-1949	-2003	mV	VRef0_DRV_reread_voltage
922)	PASS	-6995	-995	-4874	uA	VRef0_DRV_reread_current
923)	PASS	-5035	-4965	-5010	mV	VRef0_JIG_reread_voltage
924)	PASS	-5070	-4950	-4991	mV	VRef0_DRV_reread_voltage
925)	PASS	-12618	-6618	-11729	uA	VRef0_DRV_reread_current
926)	PASS	-12035	-11965	-12023	mV	VRef0_JIG_reread_voltage
927)	PASS	-12083	-11963	-11992	mV	VRef0_DRV_reread_voltage
928)	PASS	8216	14216	11188	uA	VRef1_DRV_reread_current
929)	PASS	765	835	793	mV	VRef1_JIG_reread_voltage

930)PASS	733	853	796	mVRef1_DRV_reread_voltage
931)PASS	22341	28341	24974	uAVRef1_DRV_reread_current
932)PASS	1765	1835	1794	mVRef1_JIG_reread_voltage
933)PASS	1734	1854	1797	mVRef1_DRV_reread_voltage
934)PASS	35022	41022	37694	uAVRef1_DRV_reread_current
935)PASS	2665	2735	2692	mVRef1_JIG_reread_voltage
936)PASS	2632	2752	2695	mVRef1_DRV_reread_voltage
937)PASS	43466	49466	46063	uAVRef1_DRV_reread_current
938)PASS	3265	3335	3290	mVRef1_JIG_reread_voltage
939)PASS	3230	3350	3293	mVRef1_DRV_reread_voltage
940)PASS	995	6995	4917	uAVRef1_DRV_reread_current
941)PASS	4965	5035	5000	mVRef1_JIG_reread_voltage
942)PASS	4940	5060	4989	mVRef1_DRV_reread_voltage
943)PASS	6609	12609	11648	uAVRef1_DRV_reread_current
944)PASS	11965	12035	12013	mVRef1_JIG_reread_voltage
945)PASS	11953	12073	11968	mVRef1_DRV_reread_voltage
946)PASS	-4598	1402	-1924	uAVRef1_DRV_reread_current
947)PASS	-2035	-1965	-2002	mVRef1_JIG_reread_voltage
948)PASS	-2062	-1942	-1994	mVRef1_DRV_reread_voltage
949)PASS	-6995	-995	-4777	uAVRef1_DRV_reread_current
950)PASS	-5035	-4965	-5001	mVRef1_JIG_reread_voltage
951)PASS	-5061	-4941	-4985	mVRef1_DRV_reread_voltage
952)PASS	-12599	-6599	-11847	uAVRef1_DRV_reread_current
953)PASS	-12035	-11965	-12010	mVRef1_JIG_reread_voltage
954)PASS	-12070	-11950	-11974	mVRef1_DRV_reread_voltage
955)PASS	8332	14332	11326	uAVRef2_DRV_reread_current
956)PASS	765	835	801	mVRef2_JIG_reread_voltage
957)PASS	741	861	800	mVRef2_DRV_reread_voltage
958)PASS	22495	28495	25189	uAVRef2_DRV_reread_current
959)PASS	1765	1835	1805	mVRef2_JIG_reread_voltage
960)PASS	1745	1865	1801	mVRef2_DRV_reread_voltage
961)PASS	35205	41205	37951	uAVRef2_DRV_reread_current
962)PASS	2665	2735	2706	mVRef2_JIG_reread_voltage
963)PASS	2646	2766	2700	mVRef2_DRV_reread_voltage
964)PASS	43668	49668	46487	uAVRef2_DRV_reread_current
965)PASS	3265	3335	3305	mVRef2_JIG_reread_voltage
966)PASS	3245	3365	3297	mVRef2_DRV_reread_voltage
967)PASS	1005	7005	5012	uAVRef2_DRV_reread_current
968)PASS	4965	5035	5007	mVRef2_JIG_reread_voltage
969)PASS	4947	5067	4990	mVRef2_DRV_reread_voltage
970)PASS	6618	12618	11844	uAVRef2_DRV_reread_current
971)PASS	11965	12035	12022	mVRef2_JIG_reread_voltage
972)PASS	11962	12082	11977	mVRef2_DRV_reread_voltage
973)PASS	-4588	1412	-1961	uAVRef2_DRV_reread_current
974)PASS	-2035	-1965	-1997	mVRef2_JIG_reread_voltage
975)PASS	-2057	-1937	-1991	mVRef2_DRV_reread_voltage
976)PASS	-6995	-995	-4809	uAVRef2_DRV_reread_current
977)PASS	-5035	-4965	-4997	mVRef2_JIG_reread_voltage
978)PASS	-5057	-4937	-4979	mVRef2_DRV_reread_voltage
979)PASS	-12599	-6599	-11560	uAVRef2_DRV_reread_current

980)	PASS	-12035	-11965	-12008	mV	VRef2_JIG_reread_voltage
981)	PASS	-12068	-11948	-11966	mV	VRef2_DRV_reread_voltage
982)	PASS	8236	14236	11220	uA	VRef3_DRV_reread_current
983)	PASS	765	835	794	mV	VRef3_JIG_reread_voltage
984)	PASS	734	854	794	mV	VRef3_DRV_reread_voltage
985)	PASS	22418	28418	25182	uA	VRef3_DRV_reread_current
986)	PASS	1765	1835	1798	mV	VRef3_JIG_reread_voltage
987)	PASS	1738	1858	1794	mV	VRef3_DRV_reread_voltage
988)	PASS	35128	41128	37819	uA	VRef3_DRV_reread_current
989)	PASS	2665	2735	2699	mV	VRef3_JIG_reread_voltage
990)	PASS	2639	2759	2693	mV	VRef3_DRV_reread_voltage
991)	PASS	43600	49600	46271	uA	VRef3_DRV_reread_current
992)	PASS	3265	3335	3300	mV	VRef3_JIG_reread_voltage
993)	PASS	3240	3360	3292	mV	VRef3_DRV_reread_voltage
994)	PASS	1005	7005	4916	uA	VRef3_DRV_reread_current
995)	PASS	4965	5035	5003	mV	VRef3_JIG_reread_voltage
996)	PASS	4943	5063	4984	mV	VRef3_DRV_reread_voltage
997)	PASS	6609	12609	11721	uA	VRef3_DRV_reread_current
998)	PASS	11965	12035	12020	mV	VRef3_JIG_reread_voltage
999)	PASS	11960	12080	11981	mV	VRef3_DRV_reread_voltage
1000)	PASS	-4598	1402	-1933	uA	VRef3_DRV_reread_current
1001)	PASS	-2035	-1965	-2005	mV	VRef3_JIG_reread_voltage
1002)	PASS	-2065	-1945	-1999	mV	VRef3_DRV_reread_voltage
1003)	PASS	-6995	-995	-4798	uA	VRef3_DRV_reread_current
1004)	PASS	-5035	-4965	-5007	mV	VRef3_JIG_reread_voltage
1005)	PASS	-5067	-4947	-4991	mV	VRef3_DRV_reread_voltage
1006)	PASS	-12609	-6609	-11760	uA	VRef3_DRV_reread_current
1007)	PASS	-12035	-11965	-12022	mV	VRef3_JIG_reread_voltage
1008)	PASS	-12082	-11962	-11983	mV	VRef3_DRV_reread_voltage
DEVCUR TESTS						
1101)	PASS	765	835	797	mV	DevCur0_JIG_reread_voltage
1102)	PASS	737	857	798	mV	DevCur0_DRV_reread_voltage
1103)	PASS	1765	1835	1799	mV	DevCur0_JIG_reread_voltage
1104)	PASS	1739	1859	1800	mV	DevCur0_DRV_reread_voltage
1105)	PASS	3265	3335	3298	mV	DevCur0_JIG_reread_voltage
1106)	PASS	3238	3358	3300	mV	DevCur0_DRV_reread_voltage
1107)	PASS	4965	5035	4998	mV	DevCur0_JIG_reread_voltage
1108)	PASS	4938	5058	4999	mV	DevCur0_DRV_reread_voltage
1109)	PASS	11965	12035	12003	mV	DevCur0_JIG_reread_voltage
1110)	PASS	11943	12063	12006	mV	DevCur0_DRV_reread_voltage
1111)	PASS	18930	25930	22378	uA	DevCur0_DRV_reread_current
1112)	PASS	137191	144191	140415	uA	DevCur0_DRV_reread_current
1113)	PASS	278054	285054	281054	uA	DevCur0_DRV_reread_current
1114)	PASS	765	835	798	mV	DevCur1_JIG_reread_voltage
1115)	PASS	738	858	799	mV	DevCur1_DRV_reread_voltage
1116)	PASS	1765	1835	1799	mV	DevCur1_JIG_reread_voltage
1117)	PASS	1739	1859	1798	mV	DevCur1_DRV_reread_voltage
1118)	PASS	3265	3335	3299	mV	DevCur1_JIG_reread_voltage
1119)	PASS	3239	3359	3299	mV	DevCur1_DRV_reread_voltage
1120)	PASS	4965	5035	5001	mV	DevCur1_JIG_reread_voltage

1121)	PASS	4941	5061	4998 mV	DevCur1_DRV_reread_voltage
1122)	PASS	11965	12035	12007 mV	DevCur1_JIG_reread_voltage
1123)	PASS	11947	12067	11999 mV	DevCur1_DRV_reread_voltage
1124)	PASS	18968	25968	22463 uA	DevCur1_DRV_reread_current
1125)	PASS	137267	144267	140600 uA	DevCur1_DRV_reread_current
1126)	PASS	278168	285168	281340 uA	DevCur1_DRV_reread_current
VTH and VCLK TESTS					
1201)	PASS	690	810	749 mV	Vth_DRV_reread_voltage
1202)	PASS	0	0	0 Dec	I02.0_logic_level
1203)	PASS	1	1	1 Dec	I02.0_logic_level
1204)	PASS	3690	3810	3750 mV	Vth_DRV_reread_voltage
1205)	PASS	0	0	0 Dec	I02.0_logic_level
1206)	PASS	1	1	1 Dec	I02.0_logic_level
1207)	PASS	6690	6810	6750 mV	Vth_DRV_reread_voltage
1208)	PASS	0	0	0 Dec	I02.0_logic_level
1209)	PASS	1	1	1 Dec	I02.0_logic_level
1210)	PASS	240	960	490 mV	Vclk_JIG_reread_voltage_on_Clk0
1211)	PASS	240	960	491 mV	Vclk_JIG_reread_voltage_on_Clk1
1212)	PASS	240	960	488 mV	Vclk_JIG_reread_voltage_on_Clk4
1213)	PASS	240	960	487 mV	Vclk_JIG_reread_voltage_on_Clk5
1214)	PASS	240	960	489 mV	Vclk_JIG_reread_voltage_on_Clk2
1215)	PASS	240	960	493 mV	Vclk_JIG_reread_voltage_on_Clk3
1216)	PASS	240	960	493 mV	Vclk_JIG_reread_voltage_on_Clk6
1217)	PASS	240	960	491 mV	Vclk_JIG_reread_voltage_on_Clk7
1218)	PASS	240	960	601 mV	Vclk_DRV_reread_voltage
1219)	PASS	2640	3360	2842 mV	Vclk_JIG_reread_voltage_on_Clk0
1220)	PASS	2640	3360	2841 mV	Vclk_JIG_reread_voltage_on_Clk1
1221)	PASS	2640	3360	2837 mV	Vclk_JIG_reread_voltage_on_Clk4
1222)	PASS	2640	3360	2837 mV	Vclk_JIG_reread_voltage_on_Clk5
1223)	PASS	2640	3360	2843 mV	Vclk_JIG_reread_voltage_on_Clk2
1224)	PASS	2640	3360	2843 mV	Vclk_JIG_reread_voltage_on_Clk3
1225)	PASS	2640	3360	2841 mV	Vclk_JIG_reread_voltage_on_Clk6
1226)	PASS	2640	3360	2839 mV	Vclk_JIG_reread_voltage_on_Clk7
1227)	PASS	2640	3360	3006 mV	Vclk_DRV_reread_voltage
1228)	PASS	5040	5760	5186 mV	Vclk_JIG_reread_voltage_on_Clk0
1229)	PASS	5040	5760	5184 mV	Vclk_JIG_reread_voltage_on_Clk1
1230)	PASS	5040	5760	5182 mV	Vclk_JIG_reread_voltage_on_Clk4
1231)	PASS	5040	5760	5178 mV	Vclk_JIG_reread_voltage_on_Clk5
1232)	PASS	5040	5760	5189 mV	Vclk_JIG_reread_voltage_on_Clk2
1233)	PASS	5040	5760	5187 mV	Vclk_JIG_reread_voltage_on_Clk3
1234)	PASS	5040	5760	5187 mV	Vclk_JIG_reread_voltage_on_Clk6
1235)	PASS	5040	5760	5180 mV	Vclk_JIG_reread_voltage_on_Clk7
1236)	PASS	5040	5760	5398 mV	Vclk_DRV_reread_voltage
PATTERN RAM TESTS					
1301)	PASS	0	0	0 Hex	Memory_reread
1302)	PASS	ffffffff	ffffffff	ffffffff	Hex Memory_reread
1303)	PASS	aaaaaaaa	aaaaaaaa	aaaaaaaa	Hex Memory_reread
1304)	PASS	55555555	55555555	55555555	Hex Memory_reread
1305)	PASS	a5a5a5a5	a5a5a5a5	a5a5a5a5	Hex Memory_reread
1306)	PASS	5a5a5a5a	5a5a5a5a	5a5a5a5a	Hex Memory_reread

OUTPUT TESTS

1401) PASS	9000	15000	12402	Hex PS4_as_JIG_Supply_voltage	mA
1402) PASS	300	2500	1435	Hex PS4_as_JIG_Supply_current	
1403) PASS	3250	3950	3400	mV IO0_0_JIG_reread_voltage	
1404) PASS	1	1	3401	Hex IO0_Jig_read_mask_(Drv_out_IO0_0)	
1405) PASS	3250	3950	3402	mV IO0_1_JIG_reread_voltage	
1406) PASS	2	2	3401	Hex IO0_Jig_read_mask_(Drv_out_IO0_1)	
1407) PASS	3250	3950	3401	mV IO0_2_JIG_reread_voltage	
1408) PASS	4	4	3401	Hex IO0_Jig_read_mask_(Drv_out_IO0_2)	
1409) PASS	3250	3950	3401	mV IO0_3_JIG_reread_voltage	
1410) PASS	8	8	3386	Hex IO0_Jig_read_mask_(Drv_out_IO0_3)	
1411) PASS	3250	3950	3404	mV IO0_4_JIG_reread_voltage	
1412) PASS	10	10	3406	Hex IO0_Jig_read_mask_(Drv_out_IO0_4)	
1413) PASS	3250	3950	3405	mV IO0_5_JIG_reread_voltage	
1414) PASS	20	20	3416	Hex IO0_Jig_read_mask_(Drv_out_IO0_5)	
1415) PASS	3250	3950	3421	mV IO0_6_JIG_reread_voltage	
1416) PASS	40	40	3422	Hex IO0_Jig_read_mask_(Drv_out_IO0_6)	
1417) PASS	3250	3950	3420	mV IO0_7_JIG_reread_voltage	
1418) PASS	80	80	3424	Hex IO0_Jig_read_mask_(Drv_out_IO0_7)	
1419) PASS	3250	3950	3424	mV IO1_0_JIG_reread_voltage	
1420) PASS	1	1	3424	Hex IO1_Jig_read_mask_(Drv_out_IO1_0)	
1421) PASS	3250	3950	3421	mV IO1_1_JIG_reread_voltage	
1422) PASS	2	2	3421	Hex IO1_Jig_read_mask_(Drv_out_IO1_1)	
1423) PASS	3250	3950	3425	mV IO1_2_JIG_reread_voltage	
1424) PASS	4	4	3425	Hex IO1_Jig_read_mask_(Drv_out_IO1_2)	
1425) PASS	3250	3950	3425	mV IO1_3_JIG_reread_voltage	
1426) PASS	8	8	3425	Hex IO1_Jig_read_mask_(Drv_out_IO1_3)	
1427) PASS	3250	3950	3425	mV IO1_4_JIG_reread_voltage	
1428) PASS	10	10	3356	Hex IO1_Jig_read_mask_(Drv_out_IO1_4)	
1429) PASS	3250	3950	3425	mV IO1_5_JIG_reread_voltage	
1430) PASS	20	20	3353	Hex IO1_Jig_read_mask_(Drv_out_IO1_5)	
1431) PASS	3250	3950	3354	mV IO1_6_JIG_reread_voltage	
1432) PASS	40	40	3354	Hex IO1_Jig_read_mask_(Drv_out_IO1_6)	
1433) PASS	3250	3950	3354	mV IO1_7_JIG_reread_voltage	
1434) PASS	80	80	3354	Hex IO1_Jig_read_mask_(Drv_out_IO1_7)	
1435) PASS	3250	3950	3352	mV IO2_0_JIG_reread_voltage	
1436) PASS	1	1			
1437) PASS	3250	3950			
1438) PASS	2	2			
1439) PASS	3250	3950			
1440) PASS	4	4			
1441) PASS	3250	3950			
1442) PASS	8	8			
1443) PASS	3250	3950			
1444) PASS	10	10			
1445) PASS	3250	3950			
1446) PASS	20	20			
1447) PASS	3250	3950			
1448) PASS	40	40			
1449) PASS	3250	3950			

Hex
IO2_Jig_
read_ma
sk_(Drv_
out_IO2
_0)
mVIO2_
1_JIG_re
read_volt
age

Hex
IO2_Jig_

read_mask_(Drv_out_IO2_1)
mVIO2_2_JIG_reread_voltage

Hex IO2_Jig_read_mask_(Drv_out_IO2_2)
mVIO2_3_JIG_reread_voltage

Hex IO2_Jig_read_mask_(Drv_out_IO2_3)
mVIO2_4_JIG_reread_voltage

Hex IO2_Jig_read_mask_(Drv_out_IO2_4)
mVIO2_5_JIG_reread_voltage

Hex IO2_Jig_read_mask_(Drv_out_IO2_5)
mVIO2_6_JIG_reread_voltage

Hex IO2_Jig_read_mask_(Drv_out_IO2_6)
mVIO2_7_JIG_reread_voltage

1450)	PASS	80	80	80	Hex IO2_Jig_read_mask_(Drv_out_IO2_7)
1451)	PASS	3250	3950	3353	mVIO3_0_JIG_reread_voltage
1452)	PASS	1	1	1	Hex IO3_Jig_read_mask_(Drv_out_IO3_0)
1453)	PASS	3250	3950	3352	mVIO3_1_JIG_reread_voltage
1454)	PASS	2	2	2	
1455)	PASS	3250	3950	3355	Hex IO3_Jig_read_mask_(Drv_out_IO3_1)
1456)	PASS	4	4	4	mVIO3_2_JIG_reread_voltage
1457)	PASS	3250	3950	3354	
1458)	PASS	8	8	8	Hex IO3_Jig_read_mask_(Drv_out_IO3_2)
1459)	PASS	3250	3950	3349	mVIO3_3_JIG_reread_voltage
1460)	PASS	10	10	10	
1461)	PASS	3250	3950	3354	Hex IO3_Jig_read_mask_(Drv_out_IO3_3)
1462)	PASS	20	20	20	mVIO3_4_JIG_reread_voltage
1463)	PASS	3250	3950	3352	
1464)	PASS	40	40	40	Hex IO3_Jig_read_mask_(Drv_out_IO3_4)
1465)	PASS	3250	3950	3352	mVIO3_5_JIG_reread_voltage
1466)	PASS	80	80	80	
1467)	PASS	3250	3950	3351	Hex IO3_Jig_read_mask_(Drv_out_IO3_5)
1468)	PASS	1	1	1	mVIO3_6_JIG_reread_voltage
1469)	PASS	3250	3950	3356	
1470)	PASS	2	2	2	Hex IO3_Jig_read_mask_(Drv_out_IO3_6)
1471)	PASS	3250	3950	3350	mVIO3_7_JIG_reread_voltage
1472)	PASS	4	4	4	
1473)	PASS	3250	3950	3353	Hex IO3_Jig_read_mask_(Drv_out_IO3_7)
1474)	PASS	8	8	8	mVIO4_0_JIG_reread_voltage
1475)	PASS	3250	3950	3348	
1476)	PASS	10	10	10	Hex IO4_Jig_read_mask_(Drv_out_IO4_0)
1477)	PASS	3250	3950	3351	mVIO4_1_JIG_reread_voltage
1478)	PASS	20	20	20	
1479)	PASS	3250	3950	3355	Hex IO4_Jig_read_mask_(Drv_out_IO4_1)
1480)	PASS	40	40	40	mVIO4_2_JIG_reread_voltage
1481)	PASS	3250	3950	3353	
1482)	PASS	80	80	80	Hex IO4_Jig_read_mask_(Drv_out_IO4_2)
1483)	PASS	3250	3950	3355	mVIO4_3_JIG_reread_voltage
1484)	PASS	1	1	1	
1485)	PASS	3250	3950	3356	Hex IO4_Jig_read_mask_(Drv_out_IO4_3)
1486)	PASS	2	2	2	mVIO4_4_JIG_reread_voltage
1487)	PASS	3250	3950	3357	
1488)	PASS	4	4	4	Hex IO4_Jig_read_mask_(Drv_out_IO4_4)
1489)	PASS	3250	3950	3359	mVIO4_5_JIG_reread_voltage
1490)	PASS	8	8	8	
1491)	PASS	3250	3950	3356	Hex IO4_Jig_read_mask_(Drv_out_IO4_5)
1492)	PASS	10	10	10	mVIO4_6_JIG_reread_voltage
1493)	PASS	3250	3950	3358	
1494)	PASS	20	20	20	Hex IO4_Jig_read_mask_(Drv_out_IO4_6)
1495)	PASS	3250	3950	3356	mVIO4_7_JIG_reread_voltage
1496)	PASS	40	40	40	
1497)	PASS	3250	3950	3355	Hex IO4_Jig_read_mask_(Drv_out_IO4_7)
1498)	PASS	80	80	80	mVIO5_0_JIG_reread_voltage
1499)	PASS	3250	3950	3351	Hex IO5_Jig_read_mask_(Drv_out_IO5_0)
						mVIO5_1_JIG_reread_voltage
						Hex IO5_Jig_read_mask_(Drv_out_IO5_1)
						mVIO5_2_JIG_reread_voltage
						Hex IO5_Jig_read_mask_(Drv_out_IO5_2)
						mVIO5_3_JIG_reread_voltage

Hex IO5_Jig_read_mask_(Drv_out_IO5_3)
mVIO5_4_JIG_reread_voltage

Hex IO5_Jig_read_mask_(Drv_out_IO5_4)
mVIO5_5_JIG_reread_voltage

Hex IO5_Jig_read_mask_(Drv_out_IO5_5)
mVIO5_6_JIG_reread_voltage

Hex IO5_Jig_read_mask_(Drv_out_IO5_6)
mVIO5_7_JIG_reread_voltage

Hex IO5_Jig_read_mask_(Drv_out_IO5_7)
mVIO6_0_JIG_reread_voltage

1500)	PASS	1	1	1	Hex IO6_Jig_read_mask_(Drv_out_IO6_0)
1501)	PASS	3250	3950	3353	mVIO6_1_JIG_reread_voltage
1502)	PASS	2	2	2	Hex IO6_Jig_read_mask_(Drv_out_IO6_1)
1503)	PASS	3250	3950	3358	mVIO6_2_JIG_reread_voltage
1504)	PASS	4	4	4	Hex IO6_Jig_read_mask_(Drv_out_IO6_2)
1505)	PASS	3250	3950	3352	mVIO6_3_JIG_reread_voltage
1506)	PASS	8	8	8	Hex IO6_Jig_read_mask_(Drv_out_IO6_3)
1507)	PASS	3250	3950	3356	mVIO6_4_JIG_reread_voltage
1508)	PASS	10	10	10	Hex IO6_Jig_read_mask_(Drv_out_IO6_4)
1509)	PASS	3250	3950	3356	mVIO6_5_JIG_reread_voltage
1510)	PASS	20	20	20	Hex IO6_Jig_read_mask_(Drv_out_IO6_5)
1511)	PASS	3250	3950	3357	mVIO6_6_JIG_reread_voltage
1512)	PASS	40	40	40	Hex IO6_Jig_read_mask_(Drv_out_IO6_6)
1513)	PASS	3250	3950	3353	mVIO6_7_JIG_reread_voltage
1514)	PASS	80	80	80	Hex IO6_Jig_read_mask_(Drv_out_IO6_7)
1515)	PASS	3250	3950	3354	mVIO6_8_JIG_reread_voltage
1516)	PASS	1	1	1	Hex IO6_Jig_read_mask_(Drv_out_IO6_8)
1517)	PASS	3250	3950	3356	mVIO6_9_JIG_reread_voltage
1518)	PASS	2	2	2	Hex IO6_Jig_read_mask_(Drv_out_IO6_9)
1519)	PASS	3250	3950	3357	mVIO6_10_JIG_reread_voltage
1520)	PASS	4	4	4	Hex IO6_Jig_read_mask_(Drv_out_IO6_10)
1521)	PASS	3250	3950	3350	mVIO7_0_JIG_reread_voltage
1522)	PASS	8	8	8	Hex IO7_Jig_read_mask_(Drv_out_IO7_0)
1523)	PASS	3250	3950	3354	mVIO7_1_JIG_reread_voltage
1524)	PASS	10	10	10	Hex IO7_Jig_read_mask_(Drv_out_IO7_1)
1525)	PASS	3250	3950	3355	mVIO7_2_JIG_reread_voltage
1526)	PASS	20	20	20	Hex IO7_Jig_read_mask_(Drv_out_IO7_2)
1527)	PASS	3250	3950	3354	mVIO7_3_JIG_reread_voltage
1528)	PASS	40	40	40	Hex IO7_Jig_read_mask_(Drv_out_IO7_3)
1529)	PASS	3250	3950	3356	mVIO7_4_JIG_reread_voltage
1530)	PASS	80	80	80	Hex IO7_Jig_read_mask_(Drv_out_IO7_4)
1531)	PASS	3250	3950	3357	mVIO7_5_JIG_reread_voltage
1532)	PASS	1	1	1	Hex IO7_Jig_read_mask_(Drv_out_IO7_5)
1533)	PASS	3250	3950	3359	mVIO7_6_JIG_reread_voltage
1534)	PASS	2	2	2	Hex IO7_Jig_read_mask_(Drv_out_IO7_6)
1535)	PASS	3250	3950	3356	mVIO7_7_JIG_reread_voltage
1536)	PASS	4	4	4	Hex IO7_Jig_read_mask_(Drv_out_IO7_7)
1537)	PASS	3250	3950	3358	mVIO7_8_JIG_reread_voltage
1538)	PASS	8	8	8	Hex IO7_Jig_read_mask_(Drv_out_IO7_8)
1539)	PASS	3250	3950	3352	mVIO7_9_JIG_reread_voltage
1540)	PASS	10	10	10	Hex IO7_Jig_read_mask_(Drv_out_IO7_9)
1541)	PASS	3250	3950	3360	mVIO7_10_JIG_reread_voltage
1542)	PASS	20	20	20	Hex IO7_Jig_read_mask_(Drv_out_IO7_10)
1543)	PASS	3250	3950	3357	mVIO8_0_JIG_reread_voltage
1544)	PASS	40	40	40	Hex IO8_Jig_read_mask_(Drv_out_IO8_0)
1545)	PASS	3250	3950	3357	mVIO8_1_JIG_reread_voltage
1546)	PASS	80	80	80	Hex IO8_Jig_read_mask_(Drv_out_IO8_1)
1547)	PASS	3250	3950	3353	mVIO8_2_JIG_reread_voltage
1548)	PASS	1	1	1	Hex IO8_Jig_read_mask_(Drv_out_IO8_2)
1549)	PASS	3250	3950	3356	mVIO8_3_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_1)
mVIO8_2_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_2)
mVIO8_3_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_3)
mVIO8_4_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_4)
mVIO8_5_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_5)
mVIO8_6_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_6)
mVIO8_7_JIG_reread_voltage

Hex IO8_Jig_read_mask_(Drv_out_IO8_7)
mVIO9_0_JIG_reread_voltage

Hex IO9_Jig_read_mask_(Drv_out_IO9_0)
mVIO9_1_JIG_reread_voltage

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1550 ) PASS      2      2      2 Hex I09_Jig_read_mask_(Drv_out_I09_1)
1551 ) PASS    3250    3950    3356 mV I09_2_JIG_reread_voltage
1552 ) PASS      4      4      4 Hex I09_Jig_read_mask_(Drv_out_I09_2)
1553 ) PASS    3250    3950    3354 mV I09_3_JIG_reread_voltage
1554 ) PASS      8      8      8 Hex I09_Jig_read_mask_(Drv_out_I09_3)
1555 ) PASS    3250    3950    3348 mV I09_4_JIG_reread_voltage
1556 ) PASS     10     10     10 Hex I09_Jig_read_mask_(Drv_out_I09_4)
1557 ) PASS    3250    3950    3354 mV I09_5_JIG_reread_voltage
1558 ) PASS     20     20     20 Hex I09_Jig_read_mask_(Drv_out_I09_5)
1559 ) PASS    3250    3950    3355 mV I09_6_JIG_reread_voltage
1560 ) PASS     40     40     40 Hex I09_Jig_read_mask_(Drv_out_I09_6)
1561 ) PASS    3250    3950    3353 mV I09_7_JIG_reread_voltage
1562 ) PASS     80     80     80 Hex I09_Jig_read_mask_(Drv_out_I09_7)
1563 ) PASS    3250    3950    3348 mV I010_0_JIG_reread_voltage
1564 ) PASS      1      1      1 Hex
I010_Jig_read_mask_(Drv_out_I010_0)
1565 ) PASS    3250    3950    3350 mV I010_1_JIG_reread_voltage
1566 ) PASS      2      2      2 Hex
I010_Jig_read_mask_(Drv_out_I010_1)
1567 ) PASS    3250    3950    3350 mV I010_2_JIG_reread_voltage
1568 ) PASS      4      4      4 Hex
I010_Jig_read_mask_(Drv_out_I010_2)
1569 ) PASS    3250    3950    3351 mV I010_3_JIG_reread_voltage
1570 ) PASS      8      8      8 Hex
I010_Jig_read_mask_(Drv_out_I010_3)
1571 ) PASS    3250    3950    3345 mV I010_4_JIG_reread_voltage
1572 ) PASS     10     10     10 Hex
I010_Jig_read_mask_(Drv_out_I010_4)
1573 ) PASS    3250    3950    3348 mV I010_5_JIG_reread_voltage
1574 ) PASS     20     20     20 Hex
I010_Jig_read_mask_(Drv_out_I010_5)
1575 ) PASS    3250    3950    3351 mV I010_6_JIG_reread_voltage
1576 ) PASS     40     40     40 Hex
I010_Jig_read_mask_(Drv_out_I010_6)
1577 ) PASS    3250    3950    3347 mV I010_7_JIG_reread_voltage
1578 ) PASS     80     80     80 Hex
I010_Jig_read_mask_(Drv_out_I010_7)
1579 ) PASS    3250    3950    3346 mV I011_0_JIG_reread_voltage
1580 ) PASS      1      1      1 Hex
I011_Jig_read_mask_(Drv_out_I011_0)
1581 ) PASS    3250    3950    3355 mV I011_1_JIG_reread_voltage
1582 ) PASS      2      2      2 Hex
I011_Jig_read_mask_(Drv_out_I011_1)
1583 ) PASS    3250    3950    3352 mV I011_2_JIG_reread_voltage
1584 ) PASS      4      4      4 Hex
I011_Jig_read_mask_(Drv_out_I011_2)
1585 ) PASS    3250    3950    3350 mV I011_3_JIG_reread_voltage
1586 ) PASS      8      8      8 Hex
I011_Jig_read_mask_(Drv_out_I011_3)
1587) PASS    3250    3950    3348 mV I011_4_JIG_reread_voltage

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1588) PASS          10          10          10Hex
I011_Jig_read_mask_(Drv_out_I011_4)
1589 ) PASS        3250         3950        3353 mV I011_5_JIG_reread_voltage
1590 ) PASS          20          20          20 Hex
I011_Jig_read_mask_(Drv_out_I011_5)
1591 ) PASS        3250         3950        3350 mV I011_6_JIG_reread_voltage
1592 ) PASS          40          40          40 Hex
I011_Jig_read_mask_(Drv_out_I011_6)
1593 ) PASS        3250         3950        3353 mV I011_7_JIG_reread_voltage
1594 ) PASS          80          80          80 Hex
I011_Jig_read_mask_(Drv_out_I011_7)
1595 ) PASS        3250         3950        3348 mV I012_0_JIG_reread_voltage
1596 ) PASS          1           1           1 Hex
I012_Jig_read_mask_(Drv_out_I012_0)
1597 ) PASS        3250         3950        3353 mV I012_1_JIG_reread_voltage
1598 ) PASS          2           2           2 Hex
I012_Jig_read_mask_(Drv_out_I012_1)
1599 ) PASS        3250         3950        3351 mV I012_2_JIG_reread_voltage
1600 ) PASS          4           4           4 Hex
I012_Jig_read_mask_(Drv_out_I012_2)
1601 ) PASS        3250         3950        3354 mV I012_3_JIG_reread_voltage
1602 ) PASS          8           8           8 Hex
I012_Jig_read_mask_(Drv_out_I012_3)
1603 ) PASS        3250         3950        3351 mV I012_4_JIG_reread_voltage
1604 ) PASS         10          10          10 Hex
I012_Jig_read_mask_(Drv_out_I012_4)
1605 ) PASS        3250         3950        3353 mV I012_5_JIG_reread_voltage
1606 ) PASS         20          20          20 Hex
I012_Jig_read_mask_(Drv_out_I012_5)
1607 ) PASS        3250         3950        3352 mV I012_6_JIG_reread_voltage
1608 ) PASS         40          40          40 Hex
I012_Jig_read_mask_(Drv_out_I012_6)
1609 ) PASS        3250         3950        3353 mV I012_7_JIG_reread_voltage
1610 ) PASS         80          80          80 Hex
I012_Jig_read_mask_(Drv_out_I012_7)
1611 ) PASS        3250         3950        3355 mV I013_0_JIG_reread_voltage
1612 ) PASS          1           1           1 Hex
I013_Jig_read_mask_(Drv_out_I013_0)
1613 ) PASS        3250         3950        3354 mV I013_1_JIG_reread_voltage
1614 ) PASS          2           2           2 Hex
I013_Jig_read_mask_(Drv_out_I013_1)
1615 ) PASS        3250         3950        3354 mV I013_2_JIG_reread_voltage
1616 ) PASS          4           4           4 Hex
I013_Jig_read_mask_(Drv_out_I013_2)
1617 ) PASS        3250         3950        3356 mV I013_3_JIG_reread_voltage
1618 ) PASS          8           8           8 Hex
I013_Jig_read_mask_(Drv_out_I013_3)
1619 ) PASS        3250         3950        3354 mV I013_4_JIG_reread_voltage
1620 ) PASS         10          10          10 Hex
I013_Jig_read_mask_(Drv_out_I013_4)
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1621 ) PASS      3250      3950      3356 mV I013_5_JIG_reread_voltage
1622 ) PASS           20          20          20 Hex
I013_Jig_read_mask_(Drv_out_I013_5)
1623 ) PASS      3250      3950      3359 mV I013_6_JIG_reread_voltage
1624 ) PASS           40          40          40 Hex
I013_Jig_read_mask_(Drv_out_I013_6)
1625 ) PASS      3250      3950      3356 mV I013_7_JIG_reread_voltage
1626 ) PASS           80          80          80 Hex
I013_Jig_read_mask_(Drv_out_I013_7)
1627 ) PASS      3250      3950      3363 mV I014_0_JIG_reread_voltage
1628 ) PASS           1           1           1 Hex
I014_Jig_read_mask_(Drv_out_I014_0)
1629 ) PASS      3250      3950      3363 mV I014_1_JIG_reread_voltage
1630 ) PASS           2           2           2 Hex
I014_Jig_read_mask_(Drv_out_I014_1)
1631 ) PASS      3250      3950      3363 mV I014_2_JIG_reread_voltage
1632 ) PASS           4           4           4 Hex
I014_Jig_read_mask_(Drv_out_I014_2)
1633 ) PASS      3250      3950      3360 mV I014_3_JIG_reread_voltage
1634 ) PASS           8           8           8 Hex
I014_Jig_read_mask_(Drv_out_I014_3)
1635 ) PASS      3250      3950      3360 mV I014_4_JIG_reread_voltage
1636 ) PASS          10          10          10 Hex
I014_Jig_read_mask_(Drv_out_I014_4)
1637 ) PASS      3250      3950      3361 mV I014_5_JIG_reread_voltage
1638 ) PASS          20          20          20 Hex
I014_Jig_read_mask_(Drv_out_I014_5)
1639 ) PASS      3250      3950      3363 mV I014_6_JIG_reread_voltage
1640 ) PASS          40          40          40 Hex
I014_Jig_read_mask_(Drv_out_I014_6)
1641 ) PASS      3250      3950      3362 mV I014_7_JIG_reread_voltage
1642 ) PASS          80          80          80 Hex
I014_Jig_read_mask_(Drv_out_I014_7)
1643 ) PASS      3250      3950      3351 mV I015_0_JIG_reread_voltage
1644 ) PASS           1           1           1 Hex
I015_Jig_read_mask_(Drv_out_I015_0)
1645 ) PASS      3250      3950      3356 mV I015_1_JIG_reread_voltage
1646 ) PASS           2           2           2 Hex
I015_Jig_read_mask_(Drv_out_I015_1)
1647 ) PASS      3250      3950      3352 mV I015_2_JIG_reread_voltage
1648 ) PASS           4           4           4 Hex
I015_Jig_read_mask_(Drv_out_I015_2)
1649 ) PASS      3250      3950      3356 mV I015_3_JIG_reread_voltage
1650 ) PASS           8           8           8 Hex
I015_Jig_read_mask_(Drv_out_I015_3)
1651 ) PASS      3250      3950      3349 mV I015_4_JIG_reread_voltage
1652 ) PASS          10          10          10 Hex
I015_Jig_read_mask_(Drv_out_I015_4)
1653) PASS      3250      3950      3354 mV I015_5_JIG_reread_voltage
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1654) PASS          20          20          20Hex
I015_Jig_read_mask_(Drv_out_I015_5)
1655 ) PASS      3250      3950      3352 mV I015_6_JIG_reread_voltage
1656 ) PASS        40        40        40 Hex
I015_Jig_read_mask_(Drv_out_I015_6)
1657 ) PASS      3250      3950      3351 mV I015_7_JIG_reread_voltage
1658 ) PASS        80        80        80 Hex
I015_Jig_read_mask_(Drv_out_I015_7)
1659 ) PASS      3250      3950      3351 mV I016_0_JIG_reread_voltage
1660 ) PASS         1         1         1 Hex
I016_Jig_read_mask_(Drv_out_I016_0)
1661 ) PASS      3250      3950      3356 mV I016_1_JIG_reread_voltage
1662 ) PASS         2         2         2 Hex
I016_Jig_read_mask_(Drv_out_I016_1)
1663 ) PASS      3250      3950      3352 mV I016_2_JIG_reread_voltage
1664 ) PASS         4         4         4 Hex
I016_Jig_read_mask_(Drv_out_I016_2)
1665 ) PASS      3250      3950      3352 mV I016_3_JIG_reread_voltage
1666 ) PASS         8         8         8 Hex
I016_Jig_read_mask_(Drv_out_I016_3)
1667 ) PASS      3250      3950      3353 mV I016_4_JIG_reread_voltage
1668 ) PASS        10        10        10 Hex
I016_Jig_read_mask_(Drv_out_I016_4)
1669 ) PASS      3250      3950      3352 mV I016_5_JIG_reread_voltage
1670 ) PASS        20        20        20 Hex
I016_Jig_read_mask_(Drv_out_I016_5)
1671 ) PASS      3250      3950      3357 mV I016_6_JIG_reread_voltage
1672 ) PASS        40        40        40 Hex
I016_Jig_read_mask_(Drv_out_I016_6)
1673 ) PASS      3250      3950      3354 mV I016_7_JIG_reread_voltage
1674 ) PASS        80        80        80 Hex
I016_Jig_read_mask_(Drv_out_I016_7)
1675 ) PASS      3250      3950      3348 mV I017_0_JIG_reread_voltage
1676 ) PASS         1         1         1 Hex
I017_Jig_read_mask_(Drv_out_I017_0)
1677 ) PASS      3250      3950      3349 mV I017_1_JIG_reread_voltage
1678 ) PASS         2         2         2 Hex
I017_Jig_read_mask_(Drv_out_I017_1)
1679 ) PASS      3250      3950      3351 mV I017_2_JIG_reread_voltage
1680 ) PASS         4         4         4 Hex
I017_Jig_read_mask_(Drv_out_I017_2)
1681 ) PASS      3250      3950      3350 mV I017_3_JIG_reread_voltage
1682 ) PASS         8         8         8 Hex
I017_Jig_read_mask_(Drv_out_I017_3)
1683 ) PASS      3250      3950      3348 mV I017_4_JIG_reread_voltage
1684 ) PASS        10        10        10 Hex
I017_Jig_read_mask_(Drv_out_I017_4)
1685 ) PASS      3250      3950      3349 mV I017_5_JIG_reread_voltage
1686 ) PASS        20        20        20 Hex
I017_Jig_read_mask_(Drv_out_I017_5)
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1687 ) PASS      3250      3950      3350 mV I017_6_JIG_reread_voltage
1688 ) PASS           40          40          40 Hex
I017_Jig_read_mask_(Drv_out_I017_6)
1689 ) PASS      3250      3950      3350 mV I017_7_JIG_reread_voltage
1690 ) PASS           80          80          80 Hex
I017_Jig_read_mask_(Drv_out_I017_7)
1691 ) PASS      3250      3950      3366 mV I018_0_JIG_reread_voltage
1692 ) PASS           1           1           1 Hex
I018_Jig_read_mask_(Drv_out_I018_0)
1693 ) PASS      3250      3950      3365 mV I018_1_JIG_reread_voltage
1694 ) PASS           2           2           2 Hex
I018_Jig_read_mask_(Drv_out_I018_1)
1695 ) PASS      3250      3950      3369 mV I018_2_JIG_reread_voltage
1696 ) PASS           4           4           4 Hex
I018_Jig_read_mask_(Drv_out_I018_2)
1697 ) PASS      3250      3950      3368 mV I018_3_JIG_reread_voltage
1698 ) PASS           8           8           8 Hex
I018_Jig_read_mask_(Drv_out_I018_3)
1699 ) PASS      3250      3950      3366 mV I018_4_JIG_reread_voltage
1700 ) PASS          10          10          10 Hex
I018_Jig_read_mask_(Drv_out_I018_4)
1701 ) PASS      3250      3950      3367 mV I018_5_JIG_reread_voltage
1702 ) PASS          20          20          20 Hex
I018_Jig_read_mask_(Drv_out_I018_5)
1703 ) PASS      3250      3950      3367 mV I018_6_JIG_reread_voltage
1704 ) PASS          40          40          40 Hex
I018_Jig_read_mask_(Drv_out_I018_6)
1705 ) PASS      3250      3950      3368 mV I018_7_JIG_reread_voltage
1706 ) PASS           80          80          80 Hex
I018_Jig_read_mask_(Drv_out_I018_7)
1707 ) PASS      3250      3950      3364 mV I019_0_JIG_reread_voltage
1708 ) PASS           1           1           1 Hex
I019_Jig_read_mask_(Drv_out_I019_0)
1709 ) PASS      3250      3950      3366 mV I019_1_JIG_reread_voltage
1710 ) PASS           2           2           2 Hex
I019_Jig_read_mask_(Drv_out_I019_1)
1711 ) PASS      3250      3950      3366 mV I019_2_JIG_reread_voltage
1712 ) PASS           4           4           4 Hex
I019_Jig_read_mask_(Drv_out_I019_2)
1713 ) PASS      3250      3950      3366 mV I019_3_JIG_reread_voltage
1714 ) PASS           8           8           8 Hex
I019_Jig_read_mask_(Drv_out_I019_3)
1715 ) PASS      3250      3950      3364 mV I019_4_JIG_reread_voltage
1716 ) PASS          10          10          10 Hex
I019_Jig_read_mask_(Drv_out_I019_4)
1717 ) PASS      3250      3950      3367 mV I019_5_JIG_reread_voltage
1718 ) PASS          20          20          20 Hex
I019_Jig_read_mask_(Drv_out_I019_5)
1719) PASS      3250      3950      3365 mV I019_6_JIG_reread_voltage
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1720) PASS          40          40          40Hex
I019_Jig_read_mask_(Drv_out_I019_6)
1721 ) PASS      3250      3950      3366 mV I019_7_JIG_reread_voltage
1722 ) PASS        80        80        80 Hex
I019_Jig_read_mask_(Drv_out_I019_7)
1723 ) PASS      3250      3950      3365 mV I020_0_JIG_reread_voltage
1724 ) PASS        1         1         1 Hex
I020_Jig_read_mask_(Drv_out_I020_0)
1725 ) PASS      3250      3950      3368 mV I020_1_JIG_reread_voltage
1726 ) PASS        2         2         2 Hex
I020_Jig_read_mask_(Drv_out_I020_1)
1727 ) PASS      3250      3950      3366 mV I020_2_JIG_reread_voltage
1728 ) PASS        4         4         4 Hex
I020_Jig_read_mask_(Drv_out_I020_2)
1729 ) PASS      3250      3950      3367 mV I020_3_JIG_reread_voltage
1730 ) PASS        8         8         8 Hex
I020_Jig_read_mask_(Drv_out_I020_3)
1731 ) PASS      3250      3950      3363 mV I020_4_JIG_reread_voltage
1732 ) PASS       10        10        10 Hex
I020_Jig_read_mask_(Drv_out_I020_4)
1733 ) PASS      3250      3950      3366 mV I020_5_JIG_reread_voltage
1734 ) PASS       20        20        20 Hex
I020_Jig_read_mask_(Drv_out_I020_5)
1735 ) PASS      3250      3950      3366 mV I020_6_JIG_reread_voltage
1736 ) PASS       40        40        40 Hex
I020_Jig_read_mask_(Drv_out_I020_6)
1737 ) PASS      3250      3950      3366 mV I020_7_JIG_reread_voltage
1738 ) PASS       80        80        80 Hex
I020_Jig_read_mask_(Drv_out_I020_7)
1739 ) PASS      3250      3950      3358 mV I021_0_JIG_reread_voltage
1740 ) PASS        1         1         1 Hex
I021_Jig_read_mask_(Drv_out_I021_0)
1741 ) PASS      3250      3950      3363 mV I021_1_JIG_reread_voltage
1742 ) PASS        2         2         2 Hex
I021_Jig_read_mask_(Drv_out_I021_1)
1743 ) PASS      3250      3950      3365 mV I021_2_JIG_reread_voltage
1744 ) PASS        4         4         4 Hex
I021_Jig_read_mask_(Drv_out_I021_2)
1745 ) PASS      3250      3950      3364 mV I021_3_JIG_reread_voltage
1746 ) PASS        8         8         8 Hex
I021_Jig_read_mask_(Drv_out_I021_3)
1747 ) PASS      3250      3950      3360 mV I021_4_JIG_reread_voltage
1748 ) PASS       10        10        10 Hex
I021_Jig_read_mask_(Drv_out_I021_4)
1749 ) PASS      3250      3950      3363 mV I021_5_JIG_reread_voltage
1750 ) PASS       20        20        20 Hex
I021_Jig_read_mask_(Drv_out_I021_5)
1751 ) PASS      3250      3950      3362 mV I021_6_JIG_reread_voltage
1752 ) PASS       40        40        40 Hex
I021_Jig_read_mask_(Drv_out_I021_6)
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1753 ) PASS      3250      3950      3362 mV I021_7_JIG_reread_voltage
1754 ) PASS           80           80           80 Hex
I021_Jig_read_mask_(Drv_out_I021_7)
1755 ) PASS      3250      3950      3366 mV I022_0_JIG_reread_voltage
1756 ) PASS           1           1           1 Hex
I022_Jig_read_mask_(Drv_out_I022_0)
1757 ) PASS      3250      3950      3371 mV I022_1_JIG_reread_voltage
1758 ) PASS           2           2           2 Hex
I022_Jig_read_mask_(Drv_out_I022_1)
1759 ) PASS      3250      3950      3368 mV I022_2_JIG_reread_voltage
1760 ) PASS           4           4           4 Hex
I022_Jig_read_mask_(Drv_out_I022_2)
1761 ) PASS      3250      3950      3369 mV I022_3_JIG_reread_voltage
1762 ) PASS           8           8           8 Hex
I022_Jig_read_mask_(Drv_out_I022_3)
1763 ) PASS      3250      3950      3363 mV I022_4_JIG_reread_voltage
1764 ) PASS          10          10          10 Hex
I022_Jig_read_mask_(Drv_out_I022_4)
1765 ) PASS      3250      3950      3370 mV I022_5_JIG_reread_voltage
1766 ) PASS          20          20          20 Hex
I022_Jig_read_mask_(Drv_out_I022_5)
1767 ) PASS      3250      3950      3370 mV I022_6_JIG_reread_voltage
1768 ) PASS          40          40          40 Hex
I022_Jig_read_mask_(Drv_out_I022_6)
1769 ) PASS      3250      3950      3367 mV I022_7_JIG_reread_voltage
1770 ) PASS           80           80           80 Hex
I022_Jig_read_mask_(Drv_out_I022_7)
1771 ) PASS      3250      3950      3360 mV I023_0_JIG_reread_voltage
1772 ) PASS           1           1           1 Hex
I023_Jig_read_mask_(Drv_out_I023_0)
1773 ) PASS      3250      3950      3368 mV I023_1_JIG_reread_voltage
1774 ) PASS           2           2           2 Hex
I023_Jig_read_mask_(Drv_out_I023_1)
1775 ) PASS      3250      3950      3368 mV I023_2_JIG_reread_voltage
1776 ) PASS           4           4           4 Hex
I023_Jig_read_mask_(Drv_out_I023_2)
1777 ) PASS      3250      3950      3366 mV I023_3_JIG_reread_voltage
1778 ) PASS           8           8           8 Hex
I023_Jig_read_mask_(Drv_out_I023_3)
1779 ) PASS      3250      3950      3365 mV I023_4_JIG_reread_voltage
1780 ) PASS          10          10          10 Hex
I023_Jig_read_mask_(Drv_out_I023_4)
1781 ) PASS      3250      3950      3369 mV I023_5_JIG_reread_voltage
1782 ) PASS          20          20          20 Hex
I023_Jig_read_mask_(Drv_out_I023_5)
1783 ) PASS      3250      3950      3369 mV I023_6_JIG_reread_voltage
1784 ) PASS          40          40          40 Hex
I023_Jig_read_mask_(Drv_out_I023_6)
1785) PASS      3250      3950      3367 mV I023_7_JIG_reread_voltage
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1786) PASS          80          80          80Hex
I023_Jig_read_mask_(Drv_out_I023_7)
1787 ) PASS      3250      3950      3366 mV I024_0_JIG_reread_voltage
1788 ) PASS          1          1          1 Hex
I024_Jig_read_mask_(Drv_out_I024_0)
1789 ) PASS      3250      3950      3368 mV I024_1_JIG_reread_voltage
1790 ) PASS          2          2          2 Hex
I024_Jig_read_mask_(Drv_out_I024_1)
1791 ) PASS      3250      3950      3368 mV I024_2_JIG_reread_voltage
1792 ) PASS          4          4          4 Hex
I024_Jig_read_mask_(Drv_out_I024_2)
1793 ) PASS      3250      3950      3368 mV I024_3_JIG_reread_voltage
1794 ) PASS          8          8          8 Hex
I024_Jig_read_mask_(Drv_out_I024_3)
1795 ) PASS      3250      3950      3365 mV I024_4_JIG_reread_voltage
1796 ) PASS         10         10         10 Hex
I024_Jig_read_mask_(Drv_out_I024_4)
1797 ) PASS      3250      3950      3369 mV I024_5_JIG_reread_voltage
1798 ) PASS         20         20         20 Hex
I024_Jig_read_mask_(Drv_out_I024_5)
1799 ) PASS      3250      3950      3368 mV I024_6_JIG_reread_voltage
1800 ) PASS         40         40         40 Hex
I024_Jig_read_mask_(Drv_out_I024_6)
1801 ) PASS      3250      3950      3364 mV I024_7_JIG_reread_voltage
1802 ) PASS          80          80          80 Hex
I024_Jig_read_mask_(Drv_out_I024_7)
1803 ) PASS      3250      3950      3340 mV I025_0_JIG_reread_voltage
1804 ) PASS          1          1          1 Hex
I025_Jig_read_mask_(Drv_out_I025_0)
1805 ) PASS      3250      3950      3368 mV I025_1_JIG_reread_voltage
1806 ) PASS          2          2          2 Hex
I025_Jig_read_mask_(Drv_out_I025_1)
1807 ) PASS      3250      3950      3366 mV I025_2_JIG_reread_voltage
1808 ) PASS          4          4          4 Hex
I025_Jig_read_mask_(Drv_out_I025_2)
1809 ) PASS      3250      3950      3385 mV I025_3_JIG_reread_voltage
1810 ) PASS          8          8          8 Hex
I025_Jig_read_mask_(Drv_out_I025_3)
1811 ) PASS      3250      3950      3365 mV I025_4_JIG_reread_voltage
1812 ) PASS         10         10         10 Hex
I025_Jig_read_mask_(Drv_out_I025_4)
1813 ) PASS      3250      3950      3369 mV I025_5_JIG_reread_voltage
1814 ) PASS         20         20         20 Hex
I025_Jig_read_mask_(Drv_out_I025_5)
1815 ) PASS      3250      3950      3368 mV I025_6_JIG_reread_voltage
1816 ) PASS         40         40         40 Hex
I025_Jig_read_mask_(Drv_out_I025_6)
1817 ) PASS      3250      3950      3368 mV I025_7_JIG_reread_voltage
1818 ) PASS          80          80          80 Hex
I025_Jig_read_mask_(Drv_out_I025_7)
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1819 ) PASS      3250      3950      3362 mV I026_0_JIG_reread_voltage
1820 ) PASS           1           1           1 Hex
I026_Jig_read_mask_(Drv_out_I026_0)
1821 ) PASS      3250      3950      3365 mV I026_1_JIG_reread_voltage
1822 ) PASS           2           2           2 Hex
I026_Jig_read_mask_(Drv_out_I026_1)
1823 ) PASS      3250      3950      3365 mV I026_2_JIG_reread_voltage
1824 ) PASS           4           4           4 Hex
I026_Jig_read_mask_(Drv_out_I026_2)
1825 ) PASS      3250      3950      3365 mV I026_3_JIG_reread_voltage
1826 ) PASS           8           8           8 Hex
I026_Jig_read_mask_(Drv_out_I026_3)
1827 ) PASS      3250      3950      3364 mV I026_4_JIG_reread_voltage
1828 ) PASS          10          10          10 Hex
I026_Jig_read_mask_(Drv_out_I026_4)
1829 ) PASS      3250      3950      3365 mV I026_5_JIG_reread_voltage
1830 ) PASS          20          20          20 Hex
I026_Jig_read_mask_(Drv_out_I026_5)
1831 ) PASS      3250      3950      3366 mV I026_6_JIG_reread_voltage
1832 ) PASS          40          40          40 Hex
I026_Jig_read_mask_(Drv_out_I026_6)
1833 ) PASS      3250      3950      3364 mV I026_7_JIG_reread_voltage
1834 ) PASS          80          80          80 Hex
I026_Jig_read_mask_(Drv_out_I026_7)
1835 ) PASS      3250      3950      3363 mV I027_0_JIG_reread_voltage
1836 ) PASS           1           1           1 Hex
I027_Jig_read_mask_(Drv_out_I027_0)
1837 ) PASS      3250      3950      3366 mV I027_1_JIG_reread_voltage
1838 ) PASS           2           2           2 Hex
I027_Jig_read_mask_(Drv_out_I027_1)
1839 ) PASS      3250      3950      3366 mV I027_2_JIG_reread_voltage
1840 ) PASS           4           4           4 Hex
I027_Jig_read_mask_(Drv_out_I027_2)
1841 ) PASS      3250      3950      3363 mV I027_3_JIG_reread_voltage
1842 ) PASS           8           8           8 Hex
I027_Jig_read_mask_(Drv_out_I027_3)
1843 ) PASS      3250      3950      3358 mV I027_4_JIG_reread_voltage
1844 ) PASS          10          10          10 Hex
I027_Jig_read_mask_(Drv_out_I027_4)
1845 ) PASS      3250      3950      3365 mV I027_5_JIG_reread_voltage
1846 ) PASS          20          20          20 Hex
I027_Jig_read_mask_(Drv_out_I027_5)
1847 ) PASS      3250      3950      3369 mV I027_6_JIG_reread_voltage
1848 ) PASS          40          40          40 Hex
I027_Jig_read_mask_(Drv_out_I027_6)
1849 ) PASS      3250      3950      3365 mV I027_7_JIG_reread_voltage
1850 ) PASS          80          80          80 Hex
I027_Jig_read_mask_(Drv_out_I027_7)
1851) PASS      3250      3950      3360 mV I028_0_JIG_reread_voltage
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1852) PASS          1          1          1Hex
I028_Jig_read_mask_(Drv_out_I028_0)
1853 ) PASS      3250      3950      3366 mV I028_1_JIG_reread_voltage
1854 ) PASS          2          2          2 Hex
I028_Jig_read_mask_(Drv_out_I028_1)
1855 ) PASS      3250      3950      3365 mV I028_2_JIG_reread_voltage
1856 ) PASS          4          4          4 Hex
I028_Jig_read_mask_(Drv_out_I028_2)
1857 ) PASS      3250      3950      3361 mV I028_3_JIG_reread_voltage
1858 ) PASS          8          8          8 Hex
I028_Jig_read_mask_(Drv_out_I028_3)
1859 ) PASS      3250      3950      3363 mV I028_4_JIG_reread_voltage
1860 ) PASS         10         10         10 Hex
I028_Jig_read_mask_(Drv_out_I028_4)
1861 ) PASS      3250      3950      3364 mV I028_5_JIG_reread_voltage
1862 ) PASS         20         20         20 Hex
I028_Jig_read_mask_(Drv_out_I028_5)
1863 ) PASS      3250      3950      3366 mV I028_6_JIG_reread_voltage
1864 ) PASS         40         40         40 Hex
I028_Jig_read_mask_(Drv_out_I028_6)
1865 ) PASS      3250      3950      3361 mV I028_7_JIG_reread_voltage
1866 ) PASS         80         80         80 Hex
I028_Jig_read_mask_(Drv_out_I028_7)
1867 ) PASS      3250      3950      3358 mV I029_0_JIG_reread_voltage
1868 ) PASS          1          1          1 Hex
I029_Jig_read_mask_(Drv_out_I029_0)
1869 ) PASS      3250      3950      3358 mV I029_1_JIG_reread_voltage
1870 ) PASS          2          2          2 Hex
I029_Jig_read_mask_(Drv_out_I029_1)
1871 ) PASS      3250      3950      3364 mV I029_2_JIG_reread_voltage
1872 ) PASS          4          4          4 Hex
I029_Jig_read_mask_(Drv_out_I029_2)
1873 ) PASS      3250      3950      3362 mV I029_3_JIG_reread_voltage
1874 ) PASS          8          8          8 Hex
I029_Jig_read_mask_(Drv_out_I029_3)
1875 ) PASS      3250      3950      3359 mV I029_4_JIG_reread_voltage
1876 ) PASS         10         10         10 Hex
I029_Jig_read_mask_(Drv_out_I029_4)
1877 ) PASS      3250      3950      3360 mV I029_5_JIG_reread_voltage
1878 ) PASS         20         20         20 Hex
I029_Jig_read_mask_(Drv_out_I029_5)
1879 ) PASS      3250      3950      3364 mV I029_6_JIG_reread_voltage
1880 ) PASS         40         40         40 Hex
I029_Jig_read_mask_(Drv_out_I029_6)
1881 ) PASS      3250      3950      3366 mV I029_7_JIG_reread_voltage
1882 ) PASS         80         80         80 Hex
I029_Jig_read_mask_(Drv_out_I029_7)
1883 ) PASS      3250      3950      3354 mV I030_0_JIG_reread_voltage
1884 ) PASS          1          1          1 Hex
I030_Jig_read_mask_(Drv_out_I030_0)
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1885 ) PASS      3250      3950      3362 mV I030_1_JIG_reread_voltage
1886 ) PASS           2           2           2 Hex
I030_Jig_read_mask_(Drv_out_I030_1)
1887 ) PASS      3250      3950      3363 mV I030_2_JIG_reread_voltage
1888 ) PASS           4           4           4 Hex
I030_Jig_read_mask_(Drv_out_I030_2)
1889 ) PASS      3250      3950      3358 mV I030_3_JIG_reread_voltage
1890 ) PASS           8           8           8 Hex
I030_Jig_read_mask_(Drv_out_I030_3)
1891 ) PASS      3250      3950      3357 mV I030_4_JIG_reread_voltage
1892 ) PASS          10          10          10 Hex
I030_Jig_read_mask_(Drv_out_I030_4)
1893 ) PASS      3250      3950      3356 mV I030_5_JIG_reread_voltage
1894 ) PASS          20          20          20 Hex
I030_Jig_read_mask_(Drv_out_I030_5)
1895 ) PASS      3250      3950      3361 mV I030_6_JIG_reread_voltage
1896 ) PASS          40          40          40 Hex
I030_Jig_read_mask_(Drv_out_I030_6)
1897 ) PASS      3250      3950      3359 mV I030_7_JIG_reread_voltage
1898 ) PASS          80          80          80 Hex
I030_Jig_read_mask_(Drv_out_I030_7)
1899 ) PASS      3250      3950      3364 mV I031_0_JIG_reread_voltage
1900 ) PASS           1           1           1 Hex
I031_Jig_read_mask_(Drv_out_I031_0)
1901 ) PASS      3250      3950      3365 mV I031_1_JIG_reread_voltage
1902 ) PASS           2           2           2 Hex
I031_Jig_read_mask_(Drv_out_I031_1)
1903 ) PASS      3250      3950      3368 mV I031_2_JIG_reread_voltage
1904 ) PASS           4           4           4 Hex
I031_Jig_read_mask_(Drv_out_I031_2)
1905 ) PASS      3250      3950      3367 mV I031_3_JIG_reread_voltage
1906 ) PASS           8           8           8 Hex
I031_Jig_read_mask_(Drv_out_I031_3)
1907 ) PASS      3250      3950      3364 mV I031_4_JIG_reread_voltage
1908 ) PASS          10          10          10 Hex
I031_Jig_read_mask_(Drv_out_I031_4)
1909 ) PASS      3250      3950      3367 mV I031_5_JIG_reread_voltage
1910 ) PASS          20          20          20 Hex
I031_Jig_read_mask_(Drv_out_I031_5)
1911 ) PASS      3250      3950      3368 mV I031_6_JIG_reread_voltage
1912 ) PASS          40          40          40 Hex
I031_Jig_read_mask_(Drv_out_I031_6)
1913 ) PASS      3250      3950      3367 mV I031_7_JIG_reread_voltage
1914 ) PASS          80          80          80 Hex
I031_Jig_read_mask_(Drv_out_I031_7)
1915 ) PASS      3250      3950      3362 mV I032_0_JIG_reread_voltage
1916 ) PASS           1           1           1 Hex
I032_Jig_read_mask_(Drv_out_I032_0)
1917) PASS      3250      3950      3365 mV I032_1_JIG_reread_voltage
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1918) PASS                2          2          2Hex
I032_Jig_read_mask_(Drv_out_I032_1)
1919 ) PASS            3250          3950          3367 mV I032_2_JIG_reread_voltage
1920 ) PASS                4          4          4 Hex
I032_Jig_read_mask_(Drv_out_I032_2)
1921 ) PASS            3250          3950          3363 mV I032_3_JIG_reread_voltage
1922 ) PASS                8          8          8 Hex
I032_Jig_read_mask_(Drv_out_I032_3)
1923 ) PASS            3250          3950          3361 mV I032_4_JIG_reread_voltage
1924 ) PASS               10          10          10 Hex
I032_Jig_read_mask_(Drv_out_I032_4)
1925 ) PASS            3250          3950          3363 mV I032_5_JIG_reread_voltage
1926 ) PASS               20          20          20 Hex
I032_Jig_read_mask_(Drv_out_I032_5)
1927 ) PASS            3250          3950          3360 mV I032_6_JIG_reread_voltage
1928 ) PASS               40          40          40 Hex
I032_Jig_read_mask_(Drv_out_I032_6)
1929 ) PASS            3250          3950          3362 mV I032_7_JIG_reread_voltage
1930 ) PASS               80          80          80 Hex
I032_Jig_read_mask_(Drv_out_I032_7)
1931 ) PASS            3250          3950          3364 mV I033_0_JIG_reread_voltage
1932 ) PASS                1          1          1 Hex
I033_Jig_read_mask_(Drv_out_I033_0)
1933 ) PASS            3250          3950          3368 mV I033_1_JIG_reread_voltage
1934 ) PASS                2          2          2 Hex
I033_Jig_read_mask_(Drv_out_I033_1)
1935 ) PASS            3250          3950          3369 mV I033_2_JIG_reread_voltage
1936 ) PASS                4          4          4 Hex
I033_Jig_read_mask_(Drv_out_I033_2)
1937 ) PASS            3250          3950          3369 mV I033_3_JIG_reread_voltage
1938 ) PASS                8          8          8 Hex
I033_Jig_read_mask_(Drv_out_I033_3)
1939 ) PASS            3250          3950          3365 mV I033_4_JIG_reread_voltage
1940 ) PASS               10          10          10 Hex
I033_Jig_read_mask_(Drv_out_I033_4)
1941 ) PASS            3250          3950          3368 mV I033_5_JIG_reread_voltage
1942 ) PASS               20          20          20 Hex
I033_Jig_read_mask_(Drv_out_I033_5)
1943 ) PASS            3250          3950          3367 mV I033_6_JIG_reread_voltage
1944 ) PASS               40          40          40 Hex
I033_Jig_read_mask_(Drv_out_I033_6)
1945 ) PASS            3250          3950          3372 mV I033_7_JIG_reread_voltage
1946 ) PASS               80          80          80 Hex
I033_Jig_read_mask_(Drv_out_I033_7)
1947 ) PASS            3250          3950          3376 mV I034_0_JIG_reread_voltage
1948 ) PASS                1          1          1 Hex
I034_Jig_read_mask_(Drv_out_I034_0)
1949 ) PASS            3250          3950          3378 mV I034_1_JIG_reread_voltage
1950 ) PASS                2          2          2 Hex
I034_Jig_read_mask_(Drv_out_I034_1)
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1951 ) PASS      3250      3950      3377 mV I034_2_JIG_reread_voltage
1952 ) PASS           4           4           4 Hex
I034_Jig_read_mask_(Drv_out_I034_2)
1953 ) PASS      3250      3950      3371 mV I034_3_JIG_reread_voltage
1954 ) PASS           8           8           8 Hex
I034_Jig_read_mask_(Drv_out_I034_3)
1955 ) PASS      3250      3950      3373 mV I034_4_JIG_reread_voltage
1956 ) PASS          10          10          10 Hex
I034_Jig_read_mask_(Drv_out_I034_4)
1957 ) PASS      3250      3950      3377 mV I034_5_JIG_reread_voltage
1958 ) PASS          20          20          20 Hex
I034_Jig_read_mask_(Drv_out_I034_5)
1959 ) PASS      3250      3950      3378 mV I034_6_JIG_reread_voltage
1960 ) PASS          40          40          40 Hex
I034_Jig_read_mask_(Drv_out_I034_6)
1961 ) PASS      3250      3950      3374 mV I034_7_JIG_reread_voltage
1962 ) PASS          80          80          80 Hex
I034_Jig_read_mask_(Drv_out_I034_7)

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INPUT and TRISTATE TESTS

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2001 ) PASS          1           1           1 Hex I00_Drv_read_mask_(Jig_out_I00_0)
2002 ) PASS          2           2           2 Hex I00_Drv_read_mask_(Jig_out_I00_1)
2003 ) PASS          4           4           4 Hex I00_Drv_read_mask_(Jig_out_I00_2)
2004 ) PASS          8           8           8 Hex I00_Drv_read_mask_(Jig_out_I00_3)
2005 ) PASS         10          10          10 Hex I00_Drv_read_mask_(Jig_out_I00_4)
2006 ) PASS         20          20          20 Hex I00_Drv_read_mask_(Jig_out_I00_5)
2007 ) PASS         40          40          40 Hex I00_Drv_read_mask_(Jig_out_I00_6)
2008 ) PASS         80          80          80 Hex I00_Drv_read_mask_(Jig_out_I00_7)
2009 ) PASS          1           1           1 Hex I01_Drv_read_mask_(Jig_out_I01_0)
2010 ) PASS          2           2           2 Hex I01_Drv_read_mask_(Jig_out_I01_1)
2011 ) PASS          4           4           4 Hex I01_Drv_read_mask_(Jig_out_I01_2)
2012 ) PASS          8           8           8 Hex I01_Drv_read_mask_(Jig_out_I01_3)
2013 ) PASS         10          10          10 Hex I01_Drv_read_mask_(Jig_out_I01_4)
2014 ) PASS         20          20          20 Hex I01_Drv_read_mask_(Jig_out_I01_5)
2015 ) PASS         40          40          40 Hex I01_Drv_read_mask_(Jig_out_I01_6)
2016 ) PASS         80          80          80 Hex I01_Drv_read_mask_(Jig_out_I01_7)
2017 ) PASS          1           1           1 Hex I02_Drv_read_mask_(Jig_out_I02_0)
2018 ) PASS          2           2           2 Hex I02_Drv_read_mask_(Jig_out_I02_1)
2019 ) PASS          4           4           4 Hex I02_Drv_read_mask_(Jig_out_I02_2)
2020 ) PASS          8           8           8 Hex I02_Drv_read_mask_(Jig_out_I02_3)
2021 ) PASS         10          10          10 Hex I02_Drv_read_mask_(Jig_out_I02_4)
2022 ) PASS         20          20          20 Hex I02_Drv_read_mask_(Jig_out_I02_5)
2023 ) PASS         40          40          40 Hex I02_Drv_read_mask_(Jig_out_I02_6)
2024 ) PASS         80          80          80 Hex I02_Drv_read_mask_(Jig_out_I02_7)
2025 ) PASS          1           1           1 Hex I03_Drv_read_mask_(Jig_out_I03_0)
2026 ) PASS          2           2           2 Hex I03_Drv_read_mask_(Jig_out_I03_1)
2027 ) PASS          4           4           4 Hex I03_Drv_read_mask_(Jig_out_I03_2)
2028 ) PASS          8           8           8 Hex I03_Drv_read_mask_(Jig_out_I03_3)
2029 ) PASS         10          10          10 Hex I03_Drv_read_mask_(Jig_out_I03_4)
2030 ) PASS         20          20          20 Hex I03_Drv_read_mask_(Jig_out_I03_5)
2031 ) PASS         40          40          40 Hex I03_Drv_read_mask_(Jig_out_I03_6)

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2032)	PASS	80	80	80	Hex	I03_Drv_read_mask_(Jig_out_I03_7)
2033)	PASS	1	1	1	Hex	I04_Drv_read_mask_(Jig_out_I04_0)
2034)	PASS	2	2	2	Hex	I04_Drv_read_mask_(Jig_out_I04_1)
2035)	PASS	4	4	4	Hex	I04_Drv_read_mask_(Jig_out_I04_2)
2036)	PASS	8	8	8	Hex	I04_Drv_read_mask_(Jig_out_I04_3)
2037)	PASS	10	10	10	Hex	I04_Drv_read_mask_(Jig_out_I04_4)
2038)	PASS	20	20	20	Hex	I04_Drv_read_mask_(Jig_out_I04_5)
2039)	PASS	40	40	40	Hex	I04_Drv_read_mask_(Jig_out_I04_6)
2040)	PASS	80	80	80	Hex	I04_Drv_read_mask_(Jig_out_I04_7)
2041)	PASS	1	1	1	Hex	I05_Drv_read_mask_(Jig_out_I05_0)
2042)	PASS	2	2	2	Hex	I05_Drv_read_mask_(Jig_out_I05_1)
2043)	PASS	4	4	4	Hex	I05_Drv_read_mask_(Jig_out_I05_2)
2044)	PASS	8	8	8	Hex	I05_Drv_read_mask_(Jig_out_I05_3)
2045)	PASS	10	10	10	Hex	I05_Drv_read_mask_(Jig_out_I05_4)
2046)	PASS	20	20	20	Hex	I05_Drv_read_mask_(Jig_out_I05_5)
2047)	PASS	40	40	40	Hex	I05_Drv_read_mask_(Jig_out_I05_6)
2048)	PASS	80	80	80	Hex	I05_Drv_read_mask_(Jig_out_I05_7)
2049)	PASS	1	1	1	Hex	I06_Drv_read_mask_(Jig_out_I06_0)
2050)	PASS	2	2	2	Hex	I06_Drv_read_mask_(Jig_out_I06_1)
2051)	PASS	4	4	4	Hex	I06_Drv_read_mask_(Jig_out_I06_2)
2052)	PASS	8	8	8	Hex	I06_Drv_read_mask_(Jig_out_I06_3)
2053)	PASS	10	10	10	Hex	I06_Drv_read_mask_(Jig_out_I06_4)
2054)	PASS	20	20	20	Hex	I06_Drv_read_mask_(Jig_out_I06_5)
2055)	PASS	40	40	40	Hex	I06_Drv_read_mask_(Jig_out_I06_6)
2056)	PASS	80	80	80	Hex	I06_Drv_read_mask_(Jig_out_I06_7)
2057)	PASS	1	1	1	Hex	I07_Drv_read_mask_(Jig_out_I07_0)
2058)	PASS	2	2	2	Hex	I07_Drv_read_mask_(Jig_out_I07_1)
2059)	PASS	4	4	4	Hex	I07_Drv_read_mask_(Jig_out_I07_2)
2060)	PASS	8	8	8	Hex	I07_Drv_read_mask_(Jig_out_I07_3)
2061)	PASS	10	10	10	Hex	I07_Drv_read_mask_(Jig_out_I07_4)
2062)	PASS	20	20	20	Hex	I07_Drv_read_mask_(Jig_out_I07_5)
2063)	PASS	40	40	40	Hex	I07_Drv_read_mask_(Jig_out_I07_6)
2064)	PASS	80	80	80	Hex	I07_Drv_read_mask_(Jig_out_I07_7)
2065)	PASS	1	1	1	Hex	I08_Drv_read_mask_(Jig_out_I08_0)
2066)	PASS	2	2	2	Hex	I08_Drv_read_mask_(Jig_out_I08_1)
2067)	PASS	4	4	4	Hex	I08_Drv_read_mask_(Jig_out_I08_2)
2068)	PASS	8	8	8	Hex	I08_Drv_read_mask_(Jig_out_I08_3)
2069)	PASS	10	10	10	Hex	I08_Drv_read_mask_(Jig_out_I08_4)
2070)	PASS	20	20	20	Hex	I08_Drv_read_mask_(Jig_out_I08_5)
2071)	PASS	40	40	40	Hex	I08_Drv_read_mask_(Jig_out_I08_6)
2072)	PASS	80	80	80	Hex	I08_Drv_read_mask_(Jig_out_I08_7)
2073)	PASS	1	1	1	Hex	I09_Drv_read_mask_(Jig_out_I09_0)
2074)	PASS	2	2	2	Hex	I09_Drv_read_mask_(Jig_out_I09_1)
2075)	PASS	4	4	4	Hex	I09_Drv_read_mask_(Jig_out_I09_2)
2076)	PASS	8	8	8	Hex	I09_Drv_read_mask_(Jig_out_I09_3)
2077)	PASS	10	10	10	Hex	I09_Drv_read_mask_(Jig_out_I09_4)
2078)	PASS	20	20	20	Hex	I09_Drv_read_mask_(Jig_out_I09_5)
2079)	PASS	40	40	40	Hex	I09_Drv_read_mask_(Jig_out_I09_6)
2080)	PASS	80	80	80	Hex	I09_Drv_read_mask_(Jig_out_I09_7)

2081) PASS	1	1	1Hex
IO10_Drv_read_mask_(Jig_out_IO10_0)			
2082) PASS	2	2	2Hex
IO10_Drv_read_mask_(Jig_out_IO10_1)			
2083) PASS	4	4	4Hex
IO10_Drv_read_mask_(Jig_out_IO10_2)			
2084) PASS	8	8	8Hex
IO10_Drv_read_mask_(Jig_out_IO10_3)			
2085) PASS	10	10	10Hex
IO10_Drv_read_mask_(Jig_out_IO10_4)			
2086) PASS	20	20	20Hex
IO10_Drv_read_mask_(Jig_out_IO10_5)			
2087) PASS	40	40	40Hex
IO10_Drv_read_mask_(Jig_out_IO10_6)			
2088) PASS	80	80	80Hex
IO10_Drv_read_mask_(Jig_out_IO10_7)			
2089) PASS	1	1	1Hex
IO11_Drv_read_mask_(Jig_out_IO11_0)			
2090) PASS	2	2	2Hex
IO11_Drv_read_mask_(Jig_out_IO11_1)			
2091) PASS	4	4	4Hex
IO11_Drv_read_mask_(Jig_out_IO11_2)			
2092) PASS	8	8	8Hex
IO11_Drv_read_mask_(Jig_out_IO11_3)			
2093) PASS	10	10	10Hex
IO11_Drv_read_mask_(Jig_out_IO11_4)			
2094) PASS	20	20	20Hex
IO11_Drv_read_mask_(Jig_out_IO11_5)			

2095) PASS	40	40	40Hex
IO11_Drv_read_mask_(Jig_out_IO11_6)			
2096) PASS	80	80	80Hex
IO11_Drv_read_mask_(Jig_out_IO11_7)			
2097) PASS	1	1	1Hex
IO12_Drv_read_mask_(Jig_out_IO12_0)			
2098) PASS	2	2	2Hex
IO12_Drv_read_mask_(Jig_out_IO12_1)			
2099) PASS	4	4	4Hex
IO12_Drv_read_mask_(Jig_out_IO12_2)			
2100) PASS	8	8	8Hex
IO12_Drv_read_mask_(Jig_out_IO12_3)			
2101) PASS	10	10	10Hex
IO12_Drv_read_mask_(Jig_out_IO12_4)			
2102) PASS	20	20	20Hex
IO12_Drv_read_mask_(Jig_out_IO12_5)			
2103) PASS	40	40	40Hex
IO12_Drv_read_mask_(Jig_out_IO12_6)			
2104) PASS	80	80	80Hex
IO12_Drv_read_mask_(Jig_out_IO12_7)			
2105) PASS	1	1	1Hex
IO13_Drv_read_mask_(Jig_out_IO13_0)			

2106) PASS	2	2	2Hex
IO13_Drv_read_mask_(Jig_out_IO13_1)			
2107) PASS	4	4	4Hex
IO13_Drv_read_mask_(Jig_out_IO13_2)			
2108) PASS	8	8	8Hex
IO13_Drv_read_mask_(Jig_out_IO13_3)			
2109) PASS	10	10	10Hex
IO13_Drv_read_mask_(Jig_out_IO13_4)			
2110) PASS	20	20	20Hex
IO13_Drv_read_mask_(Jig_out_IO13_5)			
2111) PASS	40	40	40Hex
IO13_Drv_read_mask_(Jig_out_IO13_6)			
2112) PASS	80	80	80Hex
IO13_Drv_read_mask_(Jig_out_IO13_7)			
2113) PASS	1	1	1Hex
IO14_Drv_read_mask_(Jig_out_IO14_0)			
2114) PASS	2	2	2Hex
IO14_Drv_read_mask_(Jig_out_IO14_1)			
2115) PASS	4	4	4Hex
IO14_Drv_read_mask_(Jig_out_IO14_2)			
2116) PASS	8	8	8Hex
IO14_Drv_read_mask_(Jig_out_IO14_3)			
2117) PASS	10	10	10Hex
IO14_Drv_read_mask_(Jig_out_IO14_4)			
2118) PASS	20	20	20Hex
IO14_Drv_read_mask_(Jig_out_IO14_5)			
2119) PASS	40	40	40Hex
IO14_Drv_read_mask_(Jig_out_IO14_6)			
2120) PASS	80	80	80Hex
IO14_Drv_read_mask_(Jig_out_IO14_7)			
2121) PASS	1	1	1Hex
IO15_Drv_read_mask_(Jig_out_IO15_0)			

2122) PASS	2	2	2Hex
IO15_Drv_read_mask_(Jig_out_IO15_1)			
2123) PASS	4	4	4Hex
IO15_Drv_read_mask_(Jig_out_IO15_2)			
2124) PASS	8	8	8Hex
IO15_Drv_read_mask_(Jig_out_IO15_3)			
2125) PASS	10	10	10Hex
IO15_Drv_read_mask_(Jig_out_IO15_4)			
2126) PASS	20	20	20Hex
IO15_Drv_read_mask_(Jig_out_IO15_5)			
2127) PASS	40	40	40Hex
IO15_Drv_read_mask_(Jig_out_IO15_6)			
2128) PASS	80	80	80Hex
IO15_Drv_read_mask_(Jig_out_IO15_7)			
2129) PASS	1	1	1Hex
IO16_Drv_read_mask_(Jig_out_IO16_0)			
2130) PASS	2	2	2Hex
IO16_Drv_read_mask_(Jig_out_IO16_1)			

2131) PASS	4	4	4Hex
IO16_Drv_read_mask_(Jig_out_IO16_2)			
2132) PASS	8	8	8Hex
IO16_Drv_read_mask_(Jig_out_IO16_3)			
2133) PASS	10	10	10Hex
IO16_Drv_read_mask_(Jig_out_IO16_4)			
2134) PASS	20	20	20Hex
IO16_Drv_read_mask_(Jig_out_IO16_5)			
2135) PASS	40	40	40Hex
IO16_Drv_read_mask_(Jig_out_IO16_6)			
2136) PASS	80	80	80Hex
IO16_Drv_read_mask_(Jig_out_IO16_7)			
2137) PASS	1	1	1Hex
IO17_Drv_read_mask_(Jig_out_IO17_0)			
2138) PASS	2	2	2Hex
IO17_Drv_read_mask_(Jig_out_IO17_1)			
2139) PASS	4	4	4Hex
IO17_Drv_read_mask_(Jig_out_IO17_2)			
2140) PASS	8	8	8Hex
IO17_Drv_read_mask_(Jig_out_IO17_3)			
2141) PASS	10	10	10Hex
IO17_Drv_read_mask_(Jig_out_IO17_4)			
2142) PASS	20	20	20Hex
IO17_Drv_read_mask_(Jig_out_IO17_5)			
2143) PASS	40	40	40Hex
IO17_Drv_read_mask_(Jig_out_IO17_6)			
2144) PASS	80	80	80Hex
IO17_Drv_read_mask_(Jig_out_IO17_7)			

2145) PASS	1	1	1Hex
IO18_Drv_read_mask_(Jig_out_IO18_0)			
2146) PASS	2	2	2Hex
IO18_Drv_read_mask_(Jig_out_IO18_1)			
2147) PASS	4	4	4Hex
IO18_Drv_read_mask_(Jig_out_IO18_2)			
2148) PASS	8	8	8Hex
IO18_Drv_read_mask_(Jig_out_IO18_3)			
2149) PASS	10	10	10Hex
IO18_Drv_read_mask_(Jig_out_IO18_4)			
2150) PASS	20	20	20Hex
IO18_Drv_read_mask_(Jig_out_IO18_5)			
2151) PASS	40	40	40Hex
IO18_Drv_read_mask_(Jig_out_IO18_6)			
2152) PASS	80	80	80Hex
IO18_Drv_read_mask_(Jig_out_IO18_7)			
2153) PASS	1	1	1Hex
IO19_Drv_read_mask_(Jig_out_IO19_0)			
2154) PASS	2	2	2Hex
IO19_Drv_read_mask_(Jig_out_IO19_1)			
2155) PASS	4	4	4Hex
IO19_Drv_read_mask_(Jig_out_IO19_2)			

2156) PASS	8	8	8Hex
IO19_Drv_read_mask_(Jig_out_IO19_3)			
2157) PASS	10	10	10Hex
IO19_Drv_read_mask_(Jig_out_IO19_4)			
2158) PASS	20	20	20Hex
IO19_Drv_read_mask_(Jig_out_IO19_5)			
2159) PASS	40	40	40Hex
IO19_Drv_read_mask_(Jig_out_IO19_6)			
2160) PASS	80	80	80Hex
IO19_Drv_read_mask_(Jig_out_IO19_7)			
2161) PASS	1	1	1Hex
IO20_Drv_read_mask_(Jig_out_IO20_0)			
2162) PASS	2	2	2Hex
IO20_Drv_read_mask_(Jig_out_IO20_1)			
2163) PASS	4	4	4Hex
IO20_Drv_read_mask_(Jig_out_IO20_2)			
2164) PASS	8	8	8Hex
IO20_Drv_read_mask_(Jig_out_IO20_3)			
2165) PASS	10	10	10Hex
IO20_Drv_read_mask_(Jig_out_IO20_4)			
2166) PASS	20	20	20Hex
IO20_Drv_read_mask_(Jig_out_IO20_5)			
2167) PASS	40	40	40Hex
IO20_Drv_read_mask_(Jig_out_IO20_6)			
2168) PASS	80	80	80Hex
IO20_Drv_read_mask_(Jig_out_IO20_7)			
2169) PASS	1	1	1Hex
IO21_Drv_read_mask_(Jig_out_IO21_0)			

2170) PASS	2	2	2Hex
IO21_Drv_read_mask_(Jig_out_IO21_1)			
2171) PASS	4	4	4Hex
IO21_Drv_read_mask_(Jig_out_IO21_2)			
2172) PASS	8	8	8Hex
IO21_Drv_read_mask_(Jig_out_IO21_3)			
2173) PASS	10	10	10Hex
IO21_Drv_read_mask_(Jig_out_IO21_4)			
2174) PASS	20	20	20Hex
IO21_Drv_read_mask_(Jig_out_IO21_5)			
2175) PASS	40	40	40Hex
IO21_Drv_read_mask_(Jig_out_IO21_6)			
2176) PASS	80	80	80Hex
IO21_Drv_read_mask_(Jig_out_IO21_7)			
2177) PASS	1	1	1Hex
IO22_Drv_read_mask_(Jig_out_IO22_0)			
2178) PASS	2	2	2Hex
IO22_Drv_read_mask_(Jig_out_IO22_1)			
2179) PASS	4	4	4Hex
IO22_Drv_read_mask_(Jig_out_IO22_2)			
2180) PASS	8	8	8Hex
IO22_Drv_read_mask_(Jig_out_IO22_3)			

2181) PASS	10	10	10Hex
IO22_Drv_read_mask_(Jig_out_IO22_4)			
2182) PASS	20	20	20Hex
IO22_Drv_read_mask_(Jig_out_IO22_5)			
2183) PASS	40	40	40Hex
IO22_Drv_read_mask_(Jig_out_IO22_6)			
2184) PASS	80	80	80Hex
IO22_Drv_read_mask_(Jig_out_IO22_7)			
2185) PASS	1	1	1Hex
IO23_Drv_read_mask_(Jig_out_IO23_0)			
2186) PASS	2	2	2Hex
IO23_Drv_read_mask_(Jig_out_IO23_1)			
2187) PASS	4	4	4Hex
IO23_Drv_read_mask_(Jig_out_IO23_2)			
2188) PASS	8	8	8Hex
IO23_Drv_read_mask_(Jig_out_IO23_3)			
2189) PASS	10	10	10Hex
IO23_Drv_read_mask_(Jig_out_IO23_4)			
2190) PASS	20	20	20Hex
IO23_Drv_read_mask_(Jig_out_IO23_5)			
2191) PASS	40	40	40Hex
IO23_Drv_read_mask_(Jig_out_IO23_6)			
2192) PASS	80	80	80Hex
IO23_Drv_read_mask_(Jig_out_IO23_7)			
2193) PASS	1	1	1Hex
IO24_Drv_read_mask_(Jig_out_IO24_0)			
2194) PASS	2	2	2Hex
IO24_Drv_read_mask_(Jig_out_IO24_1)			

2195) PASS	4	4	4Hex
IO24_Drv_read_mask_(Jig_out_IO24_2)			
2196) PASS	8	8	8Hex
IO24_Drv_read_mask_(Jig_out_IO24_3)			
2197) PASS	10	10	10Hex
IO24_Drv_read_mask_(Jig_out_IO24_4)			
2198) PASS	20	20	20Hex
IO24_Drv_read_mask_(Jig_out_IO24_5)			
2199) PASS	40	40	40Hex
IO24_Drv_read_mask_(Jig_out_IO24_6)			
2200) PASS	80	80	80Hex
IO24_Drv_read_mask_(Jig_out_IO24_7)			
2201) PASS	1	1	1Hex
IO25_Drv_read_mask_(Jig_out_IO25_0)			
2202) PASS	2	2	2Hex
IO25_Drv_read_mask_(Jig_out_IO25_1)			
2203) PASS	4	4	4Hex
IO25_Drv_read_mask_(Jig_out_IO25_2)			
2204) PASS	8	8	8Hex
IO25_Drv_read_mask_(Jig_out_IO25_3)			
2205) PASS	10	10	10Hex
IO25_Drv_read_mask_(Jig_out_IO25_4)			

2206) PASS	20	20	20Hex
IO25_Drv_read_mask_(Jig_out_IO25_5)			
2207) PASS	40	40	40Hex
IO25_Drv_read_mask_(Jig_out_IO25_6)			
2208) PASS	80	80	80Hex
IO25_Drv_read_mask_(Jig_out_IO25_7)			
2209) PASS	1	1	1Hex
IO26_Drv_read_mask_(Jig_out_IO26_0)			
2210) PASS	2	2	2Hex
IO26_Drv_read_mask_(Jig_out_IO26_1)			
2211) PASS	4	4	4Hex
IO26_Drv_read_mask_(Jig_out_IO26_2)			
2212) PASS	8	8	8Hex
IO26_Drv_read_mask_(Jig_out_IO26_3)			
2213) PASS	10	10	10Hex
IO26_Drv_read_mask_(Jig_out_IO26_4)			
2214) PASS	20	20	20Hex
IO26_Drv_read_mask_(Jig_out_IO26_5)			
2215) PASS	40	40	40Hex
IO26_Drv_read_mask_(Jig_out_IO26_6)			
2216) PASS	80	80	80Hex
IO26_Drv_read_mask_(Jig_out_IO26_7)			
2217) PASS	1	1	1Hex
IO27_Drv_read_mask_(Jig_out_IO27_0)			
2218) PASS	2	2	2Hex
IO27_Drv_read_mask_(Jig_out_IO27_1)			
2219) PASS	4	4	4Hex
IO27_Drv_read_mask_(Jig_out_IO27_2)			

2220) PASS	8	8	8Hex
IO27_Drv_read_mask_(Jig_out_IO27_3)			
2221) PASS	10	10	10Hex
IO27_Drv_read_mask_(Jig_out_IO27_4)			
2222) PASS	20	20	20Hex
IO27_Drv_read_mask_(Jig_out_IO27_5)			
2223) PASS	40	40	40Hex
IO27_Drv_read_mask_(Jig_out_IO27_6)			
2224) PASS	80	80	80Hex
IO27_Drv_read_mask_(Jig_out_IO27_7)			
2225) PASS	1	1	1Hex
IO28_Drv_read_mask_(Jig_out_IO28_0)			
2226) PASS	2	2	2Hex
IO28_Drv_read_mask_(Jig_out_IO28_1)			
2227) PASS	4	4	4Hex
IO28_Drv_read_mask_(Jig_out_IO28_2)			
2228) PASS	8	8	8Hex
IO28_Drv_read_mask_(Jig_out_IO28_3)			
2229) PASS	10	10	10Hex
IO28_Drv_read_mask_(Jig_out_IO28_4)			
2230) PASS	20	20	20Hex
IO28_Drv_read_mask_(Jig_out_IO28_5)			

2231) PASS	40	40	40Hex
IO28_Drv_read_mask_(Jig_out_IO28_6)			
2232) PASS	80	80	80Hex
IO28_Drv_read_mask_(Jig_out_IO28_7)			
2233) PASS	1	1	1Hex
IO29_Drv_read_mask_(Jig_out_IO29_0)			
2234) PASS	2	2	2Hex
IO29_Drv_read_mask_(Jig_out_IO29_1)			
2235) PASS	4	4	4Hex
IO29_Drv_read_mask_(Jig_out_IO29_2)			
2236) PASS	8	8	8Hex
IO29_Drv_read_mask_(Jig_out_IO29_3)			
2237) PASS	10	10	10Hex
IO29_Drv_read_mask_(Jig_out_IO29_4)			
2238) PASS	20	20	20Hex
IO29_Drv_read_mask_(Jig_out_IO29_5)			
2239) PASS	40	40	40Hex
IO29_Drv_read_mask_(Jig_out_IO29_6)			
2240) PASS	80	80	80Hex
IO29_Drv_read_mask_(Jig_out_IO29_7)			
2241) PASS	1	1	1Hex
IO30_Drv_read_mask_(Jig_out_IO30_0)			
2242) PASS	2	2	2Hex
IO30_Drv_read_mask_(Jig_out_IO30_1)			
2243) PASS	4	4	4Hex
IO30_Drv_read_mask_(Jig_out_IO30_2)			
2244) PASS	8	8	8Hex
IO30_Drv_read_mask_(Jig_out_IO30_3)			

2245) PASS	10	10	10Hex
IO30_Drv_read_mask_(Jig_out_IO30_4)			
2246) PASS	20	20	20Hex
IO30_Drv_read_mask_(Jig_out_IO30_5)			
2247) PASS	40	40	40Hex
IO30_Drv_read_mask_(Jig_out_IO30_6)			
2248) PASS	80	80	80Hex
IO30_Drv_read_mask_(Jig_out_IO30_7)			
2249) PASS	1	1	1Hex
IO31_Drv_read_mask_(Jig_out_IO31_0)			
2250) PASS	2	2	2Hex
IO31_Drv_read_mask_(Jig_out_IO31_1)			
2251) PASS	4	4	4Hex
IO31_Drv_read_mask_(Jig_out_IO31_2)			
2252) PASS	8	8	8Hex
IO31_Drv_read_mask_(Jig_out_IO31_3)			
2253) PASS	10	10	10Hex
IO31_Drv_read_mask_(Jig_out_IO31_4)			
2254) PASS	20	20	20Hex
IO31_Drv_read_mask_(Jig_out_IO31_5)			
2255) PASS	40	40	40Hex
IO31_Drv_read_mask_(Jig_out_IO31_6)			

2256) PASS	80	80	80Hex
IO31_Drv_read_mask_(Jig_out_IO31_7)			
2257) PASS	1	1	1Hex
IO32_Drv_read_mask_(Jig_out_IO32_0)			
2258) PASS	2	2	2Hex
IO32_Drv_read_mask_(Jig_out_IO32_1)			
2259) PASS	4	4	4Hex
IO32_Drv_read_mask_(Jig_out_IO32_2)			
2260) PASS	8	8	8Hex
IO32_Drv_read_mask_(Jig_out_IO32_3)			
2261) PASS	10	10	10Hex
IO32_Drv_read_mask_(Jig_out_IO32_4)			
2262) PASS	20	20	20Hex
IO32_Drv_read_mask_(Jig_out_IO32_5)			
2263) PASS	40	40	40Hex
IO32_Drv_read_mask_(Jig_out_IO32_6)			
2264) PASS	80	80	80Hex
IO32_Drv_read_mask_(Jig_out_IO32_7)			
2265) PASS	1	1	1Hex
IO33_Drv_read_mask_(Jig_out_IO33_0)			
2266) PASS	2	2	2Hex
IO33_Drv_read_mask_(Jig_out_IO33_1)			
2267) PASS	4	4	4Hex
IO33_Drv_read_mask_(Jig_out_IO33_2)			
2268) PASS	8	8	8Hex
IO33_Drv_read_mask_(Jig_out_IO33_3)			
2269) PASS	10	10	10Hex
IO33_Drv_read_mask_(Jig_out_IO33_4)			
2270) PASS	20	20	20Hex
IO33_Drv_read_mask_(Jig_out_IO33_5)			
2271) PASS	40	40	40Hex
IO33_Drv_read_mask_(Jig_out_IO33_6)			

2272) PASS	80	80	80Hex
IO33_Drv_read_mask_(Jig_out_IO33_7)			
2273) PASS	1	1	1Hex
IO34_Drv_read_mask_(Jig_out_IO34_0)			
2274) PASS	2	2	2Hex
IO34_Drv_read_mask_(Jig_out_IO34_1)			
2275) PASS	4	4	4Hex
IO34_Drv_read_mask_(Jig_out_IO34_2)			
2276) PASS	8	8	8Hex
IO34_Drv_read_mask_(Jig_out_IO34_3)			
2277) PASS	10	10	10Hex
IO34_Drv_read_mask_(Jig_out_IO34_4)			
2278) PASS	20	20	20Hex
IO34_Drv_read_mask_(Jig_out_IO34_5)			
2279) PASS	40	40	40Hex
IO34_Drv_read_mask_(Jig_out_IO34_6)			
2280) PASS	80	80	80Hex
IO34_Drv_read_mask_(Jig_out_IO34_7)			

WAVEGEN TESTS

0 TESTS

Test# PASS/MinMaxReadDescription

.....	.FAIL	...	Value	...	Value	...	Value.....
2401)	PASS	0		0		0	Dec Lvpps_error_code
2402)	PASS	11900		12100		12000	mV PS0_voltage(mV).
2403)	PASS	11900		12100		12000	mV PS1_voltage(mV).
2404)	PASS	11900		12100		12000	mV PS2_voltage(mV).
2405)	PASS	11900		12100		12000	mV PS3_voltage(mV).
2406)	PASS	9000		15000		12001	mV PS1_as_JIG_Supply_voltage
2407)	PASS	300		2500		1471	mA PS1_as_JIG_Supply_current
2408)	PASS	11900		12100		12000	mV PS4_voltage(mV).
2409)	PASS	11900		12100		12000	mV PS5_voltage(mV).
2410)	PASS	5975		6025		6000	mV VRef0_voltage(mV)
2411)	PASS	5975		6025		6000	mV VRef1_voltage(mV)
2412)	PASS	5975		6025		6000	mV VRef2_voltage(mV)
2413)	PASS	5975		6025		6000	mV VRef3_voltage(mV)
2414)	PASS	5975		6025		6000	mV DevCur0_voltage(mV)
2415)	PASS	5975		6025		6000	mV DevCur1_voltage(mV)
2416)	PASS	970000		1030000		1000000	Hz Clock_Frequency_(Hz)
2417)	PASS	4600		5400		5000	mV Clock_amplitude

All test passed. Driver CALIBRATION DATE UPDATED. Test log end