```
*************************************************************************
************************* SYSTEM TEST SECTION ************************
*************************************************************************


-------------------------------------------------------------------------

Specials (SEE CORC):
--------------------
1) Is there a Customer Acceptance (CA) required on this system? Yes
- if YES, see APPENDIX Z

2) Were Special IP/Internet addresses added/executed? No

3) Is there a special system configuration? No

4) Software:
Default Version: 7.0D7
Current Version: No

Software Bugs
-------------
First Bug Number : ICDgu60517
Checker Name     : TMS_ST
IMAGE Version    : 7.0 D7

Second Bug Number: None
Checker Name     : None
IMAGE Version    : None

Failing Data:
------------


This failure is Intermittent.

Tmsalst checker fails pulsewidth test :

Sequencer : ck_tms_quick_cap

411043 pulsewitdth  ck_tms_pw  248.0 nS <  254.4 nS (F) < 254.0 nS
411044 pulsewitdth  ck_tms_pw  248.0 nS <  254.4 nS (F) < 254.0 nS
41104  PW ram       ck_tms_pw  248.0 nS <  254.5 nS (F) < 254.0 nS



*************************************************************************
```

System Issues
----
1) Issues with system (i.e. if hardware is replaced within 3 days of shipping please specify details. Is order shipping complete? Notes from system log book, etc.):
- None

2) Issues with system at button-up (i.e. wires decabled accidently and placed back without electrical verification,
mechanical issues, etc ...):
- None

3) Miscellaneous notes (Include any new options shipping, eg VHFDIG, SSSP):
- None.

Configuration File was verified by:
- Sam Baliamoune

#**#

```
#
# Confsim file created on: 03/02/04 08:55:39
#
# Catalyst tester nrtester87t
#
# nrtester87t is a 2 processor system
#   Processor 1: 450 MHz sparcv9 (online)
#   Processor 3: 450 MHz sparcv9 (online)
# PCI based system
# Terabus is present
#    Board ID      949-824-00
#    Serial number 03038642
#    Revison Date  0103-0
#    Artwork       E
# TCI is present
#


CATALYST_TH 1
BACKPLANE A
#Slot Type        Serial #   Rev  Num    Date   XptA XptB Name
2   879-858-35  0x3001404   B   0    # 0131-0 23   24   PLFD   CC
3   879-858-25  0x0128abf   A   0    # 9625-0 21   22   PLFS   CC
4   000-000-00  0x0000000  P0   0    # 0000-0 19   20   EMPTY
5   879-792-01  0x2003086   A   0    # 0030-0 17   18   TIME   CC
6   000-000-00  0x0000000  P0   0    # 0000-0 15   16   EMPTY
7   000-000-00  0x0000000  P0   0    # 0000-0 13   14   EMPTY
8   000-000-00  0x0000000  P0   0    # 0000-0 11   12   EMPTY
9   000-000-00  0x0000000  P0   0    # 0000-0 9    10   EMPTY
10   949-681-50  0x3002011   B   0    # 0404-0 7    8    VHFAWG400 DIFF CC
```

```
11   949-672-50  0x0254feb   D   0    # 0107-0 5    6    VHFDIG CC
14   000-000-00  0x0000000  P0   0    # 0000-0 25   26   EMPTY
15   000-000-00  0x0000000  P0   0    # 0000-0 27   28   EMPTY
16   000-000-00  0x0000000  P0   0    # 0000-0 29   30   EMPTY
17   000-000-00  0x0000000  P0   0    # 0000-0 31   32   EMPTY
18   000-000-00  0x0000000  P0   0    # 0000-0 33   34   EMPTY
19   879-858-23  0x303ab5f   B   0    # 0131-0 35   36   REFSRC CC
20   000-000-00  0x0000000  P0   0    # 0000-0 37   38   EMPTY
21   000-000-00  0x0000000  P0   0    # 0000-0 39   40   EMPTY
22   000-000-00  0x0000000  P0   0    # 0000-0 41   42   EMPTY
23   000-000-00  0x0000000  P0   0    # 0000-0 43   44   EMPTY
1   949-669-00  0x200245c   B   0    # 9825-0 3    0    HAS (Left HAS
LA669-00)
13   949-668-00  0x2001d04   A   0    # 9530-0 0    0    CATALYST HAC
12   949-667-00  0x2001a69   D   0    # 0102-0 0    0    DIF
24   949-669-01  0x2001c19   B   0    # 9717-0 4    0    HAS (Right HAS
LA669-01)
END


RF_PIPES 1
# pipe-name slot schan description
END


#
# Up to 4 Precision AC Card Cages are allowed
#
PRECISION_AC 1
#Slot Type        Serial #   Rev  Num    Date   Name
1   949-718-00  0x303a523   C   0    # 0331-0 PLFDIG TCI
2   879-764-01  0x802ff85   F   0    # 0044-0 PLFSRC
3   879-779-00  0x802ff85   F   0    # 0044-0 1M SMEM
4   949-664-00  0x01ebc81   C   0    # 0113-0 VHFDIG MF
5   949-827-00  0x3001c9b   B   0    # 0149-0 VHFAWG
6   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
7   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
8   949-671-00  0x3039322   E   0    # 9822-0 PACS CAGE INT
END

PRECISION_AC 2
#Slot Type        Serial #   Rev  Num    Date   Name
1   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
2   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
3   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
4   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
5   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
6   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
7   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
8   949-671-00  0x303932a   E   0    # 9822-0 PACS CAGE INT
END
```

```
#
# Up to 8 Universal Backplane/Synch Power Subsystem
# cages are allowed
#
# For the Synch Power Subsystem:
# Slot   Type       Name          Instr1   #    Instr2  #  Ammeter   #
#
#        Instr1  #   - insrument connected to the first two matrix lines
#        Instr2  #   - insrument connected to the last  two matrix lines
#        Ammeter #         - ammeter connection
#        to AVOID errors, put NO 0 if no instrument is connected.
#
#
UB_SPS_CAGE 1
# Slot   Type     Serial #   Code Num    Date   Name
1   879-802-02  0x200216d   F   0    # 9817-0 UB_SPS_802
2   517-301-00  0x0261001   B   0    # 0229-0 UB_APU
3   517-301-00  0x025ff21   B   0    # 0229-0 UB_APU
4   517-301-00  0x0260ffe   B   0    # 0229-0 UB_APU
5   517-301-00  0x025ff45   B   0    # 0229-0 UB_APU
6   517-301-00  0x025ff3f   B   0    # 0229-0 UB_APU
7   517-301-00  0x025ff1c   B   0    # 0229-0 UB_APU
8   517-301-01  0x000b9da   B   0    # 0216-0 UB_MATRIX
9   517-301-01  0x000b05d   B   0    # 0216-0 UB_MATRIX
10   517-301-01  0x000c3aa   B   0    # 0216-0 UB_MATRIX
11   517-301-01  0x000c3b1   B   0    # 0216-0 UB_MATRIX
12   517-301-01  0x000b044   B   0    # 0216-0 UB_MATRIX
13   517-301-01  0x000c3b0   B   0    # 0216-0 UB_MATRIX
14   879-925-01  0x3039565   D   0    # 0317-0 UB_60_V_SRC MAT 1
15   879-925-01  0x3039a11   D   0    # 0317-0 UB_60_V_SRC DUT 1
16   879-925-01  0x3039900   D   0    # 0317-0 UB_60_V_SRC MAT 2
17   879-925-01  0x30398f4   D   0    # 0317-0 UB_60_V_SRC DUT 2
21   879-690-00  0x2001264   B   0    # 0128-0 UB_ASY
22   517-300-01  0x026070d   B   0    # 0237-0 UB_TJ300
END



HSD100_CHAN_CAGE  1
#Slot Type        Serial #   Rev  Num  Fld1   Fld2    Date    Name
1   949-921-01  0x3000a85   B   0                # 0309-0  HSD CDM 400
2   949-921-01  0x3038fe8   B   0                # 0309-0  HSD CDM 400
3   949-921-01  0x8036bfc   B   0                # 0309-0  HSD CDM 400
4   949-921-01  0x8036b96   B   0                # 0309-0  HSD CDM 400
5   949-920-10  0x3001e24   C   0                # 0323-0  HSD CSB
6   949-921-01  0x800e95b   B   0                # 9948-0  HSD CDM 400
7   949-921-01  0x3000a99   B   0                # 0309-0  HSD CDM 400
8   949-921-01  0x3000a91   B   0                # 0309-0  HSD CDM 400
9   949-921-01  0x3038faf   B   0                # 0309-0  HSD CDM 400
END



HSD100_CHAN_CAGE  2
```

```
#Slot Type        Serial #   Rev  Num  Fld1   Fld2    Date    Name
5   949-820-00  0x3039743   B   0                # 0043-0  HSD CSB
END


CATALYST_TH 1
BACKPLANE B
#Slot Type        Serial #   Rev  Num    Date   Name
29   949-626-10  0x303a809   B   0    # 0044-0 HSD THS
43   949-625-00  0x303a95d   D   0    # 0045-0 HSD DTH
44   949-625-00  0x303a98e   D   0    # 0045-0 HSD DTH
45   949-625-00  0x303a926   D   0    # 0045-0 HSD DTH
46   949-625-00  0x303a940   D   0    # 0045-0 HSD DTH
47   949-626-10  0x303a7e7   B   0    # 0044-0 HSD THS
48   949-625-00  0x30393f3   D   0    # 0045-0 HSD DTH
49   949-625-00  0x303a95e   D   0    # 0045-0 HSD DTH
50   949-625-00  0x303a8fd   D   0    # 0045-0 HSD DTH
51   949-625-00  0x303948b   D   0    # 0045-0 HSD DTH
61   949-625-00  0x303a950   D   0    # 0045-0 HSD DTH
62   949-625-00  0x303a941   D   0    # 0045-0 HSD DTH
63   949-625-00  0x303a93c   D   0    # 0045-0 HSD DTH
64   949-625-00  0x303a95b   D   0    # 0045-0 HSD DTH
65   949-626-00  0x30397b5   A   0    # 0044-0 HSD THS
66   949-625-00  0x30390bf   D   0    # 0045-0 HSD DTH
67   949-625-00  0x303a969   D   0    # 0045-0 HSD DTH
68   949-625-00  0x303a94d   D   0    # 0045-0 HSD DTH
69   949-625-00  0x303a916   D   0    # 0045-0 HSD DTH
74   949-626-10  0x303a808   B   0    # 0044-0 HSD THS
END


#
# Time Subsystem
#
TIME_SUBSYSTEM
#Slot Board ID    Serial #   Rev  Num    Date   Name
X   879-793-00  0x30395B6   D   0    # 9923-0 TMS Timer
X   879-794-01  0x20020B4   B   0    # 9206-0 TMS Counter
X   879-795-01  0x2002CBB   B   0    # 8751-0 TMS Support
END


#
# DC Subsystem -
#
#    SRC <NUM>                  [1 - 13]
#      (sources 1-5 are MATRIX sources 1-5
#       sources 6-13 are DUT sources 1-8)
#    HCU <NUM>                 *[1 - 4]
#    REF HCU <NUM>             *[1 - 4]
#    HVSRC <NUM>               *[1 - 4]
#    PWRSRC <NUM>               [1 - 4]
#    DATABITS <NUM> - <NUM>     [1 - 192]
```

```
#
# ** These instruments share the same seven-slot cage -- only one
#    instrument is allowed per slot.
#
DC_SUBSYSTEM
#  UBVI 60   1     ( 60V V/I Source in Universal Backplane 1 : slot 14)
#  UBVI 60   2     ( 60V V/I Source in Universal Backplane 1 : slot 16)
HCU       4
#  UBVI 60   6     ( 60V V/I Source in Universal Backplane 1 : slot 15)
#  UBVI 60   7     ( 60V V/I Source in Universal Backplane 1 : slot 17)
HCU       8
HCU       9
DATABITS  1 - 48

#  UB_MATRIX
#
#  Testhead 1
#  XPTs     UB Cage  Slot     Type
#  1-4      1        2        APU
#  5-8      1        3        APU
#  9-12     1        4        APU
#  13-16    1        5        APU
#  17-20    1        6        APU
#  21-24    1        7        APU
#  25-28    1        8        Matrix
#  29-32    1        9        Matrix
#  33-36    1        10       Matrix
#  37-40    1        11       Matrix
#  41-44    1        12       Matrix
#  45-48    1        13       Matrix
END



#
# Confsim file created on: 03/02/04 08:55:39
#
# Catalyst tester nrtester87t
#
# nrtester87t is a 2 processor system
#   Processor 1: 450 MHz sparcv9 (online)
#   Processor 3: 450 MHz sparcv9 (online)
# PCI based system
# Terabus is present
#    Board ID      949-824-00
#    Serial number 03038642
#    Revison Date  0103-0
#    Artwork       E
# TCI is present
#



CATALYST_TH 1
BACKPLANE A
```

```
#Slot Type        Serial #   Rev  Num    Date   XptA XptB Name
2   879-858-35  0x3001404   B   0    # 0131-0 23   24   PLFD   CC
3   879-858-25  0x0128abf   A   0    # 9625-0 21   22   PLFS   CC
4   000-000-00  0x0000000  P0   0    # 0000-0 19   20   EMPTY
5   879-792-01  0x2003086   A   0    # 0030-0 17   18   TIME   CC
6   000-000-00  0x0000000  P0   0    # 0000-0 15   16   EMPTY
7   000-000-00  0x0000000  P0   0    # 0000-0 13   14   EMPTY
8   000-000-00  0x0000000  P0   0    # 0000-0 11   12   EMPTY
9   000-000-00  0x0000000  P0   0    # 0000-0 9    10   EMPTY
10   949-681-50  0x3002011   B   0    # 0404-0 7    8    VHFAWG400 DIFF CC
11   949-672-50  0x0254feb   D   0    # 0107-0 5    6    VHFDIG CC
14   000-000-00  0x0000000  P0   0    # 0000-0 25   26   EMPTY
15   000-000-00  0x0000000  P0   0    # 0000-0 27   28   EMPTY
16   000-000-00  0x0000000  P0   0    # 0000-0 29   30   EMPTY
17   000-000-00  0x0000000  P0   0    # 0000-0 31   32   EMPTY
18   000-000-00  0x0000000  P0   0    # 0000-0 33   34   EMPTY
19   879-858-23  0x303ab5f   B   0    # 0131-0 35   36   REFSRC CC
20   000-000-00  0x0000000  P0   0    # 0000-0 37   38   EMPTY
21   000-000-00  0x0000000  P0   0    # 0000-0 39   40   EMPTY
22   000-000-00  0x0000000  P0   0    # 0000-0 41   42   EMPTY
23   000-000-00  0x0000000  P0   0    # 0000-0 43   44   EMPTY
1   949-669-00  0x200245c   B   0    # 9825-0 3    0    HAS (Left HAS
LA669-00)
13   949-668-00  0x2001d04   A   0    # 9530-0 0    0    CATALYST HAC
12   949-667-00  0x2001a69   D   0    # 0102-0 0    0    DIF
24   949-669-01  0x2001c19   B   0    # 9717-0 4    0    HAS (Right HAS
LA669-01)
END


RF_PIPES 1
# pipe-name slot schan description
END


#
# Up to 4 Precision AC Card Cages are allowed
#
PRECISION_AC 1
#Slot Type        Serial #   Rev  Num    Date   Name
1   949-718-00  0x303a523   C   0    # 0331-0 PLFDIG TCI
2   879-764-01  0x802ff85   F   0    # 0044-0 PLFSRC
3   879-779-00  0x802ff85   F   0    # 0044-0 1M SMEM
4   949-664-00  0x01ebc81   C   0    # 0113-0 VHFDIG MF
5   949-827-00  0x3001c9b   B   0    # 0149-0 VHFAWG
6   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
7   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
8   949-671-00  0x3039322   E   0    # 9822-0 PACS CAGE INT
END

PRECISION_AC 2
#Slot Type        Serial #   Rev  Num    Date   Name
1   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
```

```
2   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
3   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
4   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
5   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
6   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
7   000-000-00  0x0000000  P0   0    # 0000-0 EMPTY
8   949-671-00  0x303932a   E   0    # 9822-0 PACS CAGE INT
END


#
# Up to 8 Universal Backplane/Synch Power Subsystem
# cages are allowed
#
# For the Synch Power Subsystem:
# Slot   Type       Name          Instr1   #    Instr2  #  Ammeter   #
#
#        Instr1  #   - insrument connected to the first two matrix lines
#        Instr2  #   - insrument connected to the last  two matrix lines
#        Ammeter #         - ammeter connection
#        to AVOID errors, put NO 0 if no instrument is connected.
#
#
UB_SPS_CAGE 1
# Slot   Type     Serial #   Code Num    Date   Name
1   879-802-02  0x200216d   F   0    # 9817-0 UB_SPS_802
2   517-301-00  0x0261001   B   0    # 0229-0 UB_APU
3   517-301-00  0x025ff21   B   0    # 0229-0 UB_APU
4   517-301-00  0x0260ffe   B   0    # 0229-0 UB_APU
5   517-301-00  0x025ff45   B   0    # 0229-0 UB_APU
6   517-301-00  0x025ff3f   B   0    # 0229-0 UB_APU
7   517-301-00  0x025ff1c   B   0    # 0229-0 UB_APU
8   517-301-01  0x000b9da   B   0    # 0216-0 UB_MATRIX
9   517-301-01  0x000b05d   B   0    # 0216-0 UB_MATRIX
10   517-301-01  0x000c3aa   B   0    # 0216-0 UB_MATRIX
11   517-301-01  0x000c3b1   B   0    # 0216-0 UB_MATRIX
12   517-301-01  0x000b044   B   0    # 0216-0 UB_MATRIX
13   517-301-01  0x000c3b0   B   0    # 0216-0 UB_MATRIX
14   879-925-01  0x3039565   D   0    # 0317-0 UB_60_V_SRC MAT 1
15   879-925-01  0x3039a11   D   0    # 0317-0 UB_60_V_SRC DUT 1
16   879-925-01  0x3039900   D   0    # 0317-0 UB_60_V_SRC MAT 2
17   879-925-01  0x30398f4   D   0    # 0317-0 UB_60_V_SRC DUT 2
21   879-690-00  0x2001264   B   0    # 0128-0 UB_ASY
22   517-300-01  0x026070d   B   0    # 0237-0 UB_TJ300
END



HSD100_CHAN_CAGE  1
#Slot Type        Serial #   Rev  Num  Fld1   Fld2    Date    Name
1   949-921-01  0x3000a85   B   0                # 0309-0  HSD CDM 400
2   949-921-01  0x3038fe8   B   0                # 0309-0  HSD CDM 400
3   949-921-01  0x8036bfc   B   0                # 0309-0  HSD CDM 400
```

```
4   949-921-01  0x8036b96   B   0                # 0309-0  HSD CDM 400
5   949-920-10  0x3001e24   C   0                # 0323-0  HSD CSB
6   949-921-01  0x800e95b   B   0                # 9948-0  HSD CDM 400
7   949-921-01  0x3000a99   B   0                # 0309-0  HSD CDM 400
8   949-921-01  0x3000a91   B   0                # 0309-0  HSD CDM 400
9   949-921-01  0x3038faf   B   0                # 0309-0  HSD CDM 400
END


HSD100_CHAN_CAGE  2
#Slot Type        Serial #   Rev  Num  Fld1   Fld2    Date    Name
5   949-820-00  0x3039743   B   0                # 0043-0  HSD CSB
END


CATALYST_TH 1
BACKPLANE B
#Slot Type        Serial #   Rev  Num    Date   Name
29   949-626-10  0x303a809   B   0    # 0044-0 HSD THS
43   949-625-00  0x303a95d   D   0    # 0045-0 HSD DTH
44   949-625-00  0x303a98e   D   0    # 0045-0 HSD DTH
45   949-625-00  0x303a926   D   0    # 0045-0 HSD DTH
46   949-625-00  0x303a940   D   0    # 0045-0 HSD DTH
47   949-626-10  0x303a7e7   B   0    # 0044-0 HSD THS
48   949-625-00  0x30393f3   D   0    # 0045-0 HSD DTH
49   949-625-00  0x303a95e   D   0    # 0045-0 HSD DTH
50   949-625-00  0x303a8fd   D   0    # 0045-0 HSD DTH
51   949-625-00  0x303948b   D   0    # 0045-0 HSD DTH
61   949-625-00  0x303a950   D   0    # 0045-0 HSD DTH
62   949-625-00  0x303a941   D   0    # 0045-0 HSD DTH
63   949-625-00  0x303a93c   D   0    # 0045-0 HSD DTH
64   949-625-00  0x303a95b   D   0    # 0045-0 HSD DTH
65   949-626-00  0x30397b5   A   0    # 0044-0 HSD THS
66   949-625-00  0x30390bf   D   0    # 0045-0 HSD DTH
67   949-625-00  0x303a969   D   0    # 0045-0 HSD DTH
68   949-625-00  0x303a94d   D   0    # 0045-0 HSD DTH
69   949-625-00  0x303a916   D   0    # 0045-0 HSD DTH
74   949-626-10  0x303a808   B   0    # 0044-0 HSD THS
END


#
# Time Subsystem
#
TIME_SUBSYSTEM
#Slot Board ID    Serial #   Rev  Num    Date   Name
X   879-793-00  0x30395B6   D   0    # 9923-0 TMS Timer
X   879-794-01  0x20020B4   B   0    # 9206-0 TMS Counter
X   879-795-01  0x2002CBB   B   0    # 8751-0 TMS Support
END
```

```
#
# DC Subsystem -
#
#    SRC <NUM>                  [1 - 13]
#      (sources 1-5 are MATRIX sources 1-5
#       sources 6-13 are DUT sources 1-8)
#    HCU <NUM>                 *[1 - 4]
#    REF HCU <NUM>             *[1 - 4]
#    HVSRC <NUM>               *[1 - 4]
#    PWRSRC <NUM>               [1 - 4]
#    DATABITS <NUM> - <NUM>     [1 - 192]
#
# ** These instruments share the same seven-slot cage -- only one
#    instrument is allowed per slot.
#
DC_SUBSYSTEM
#  UBVI 60   1     ( 60V V/I Source in Universal Backplane 1 : slot 14)
#  UBVI 60   2     ( 60V V/I Source in Universal Backplane 1 : slot 16)
HCU       4
#  UBVI 60   6     ( 60V V/I Source in Universal Backplane 1 : slot 15)
#  UBVI 60   7     ( 60V V/I Source in Universal Backplane 1 : slot 17)
HCU       8
HCU       9
DATABITS  1 - 48

#  UB_MATRIX
#
#  Testhead 1
#  XPTs     UB Cage  Slot     Type
#  1-4      1        2        APU
#  5-8      1        3        APU
#  9-12     1        4        APU
#  13-16    1        5        APU
#  17-20    1        6        APU
#  21-24    1        7        APU
#  25-28    1        8        Matrix
#  29-32    1        9        Matrix
#  33-36    1        10       Matrix
#  37-40    1        11       Matrix
#  41-44    1        12       Matrix
#  45-48    1        13       Matrix
```